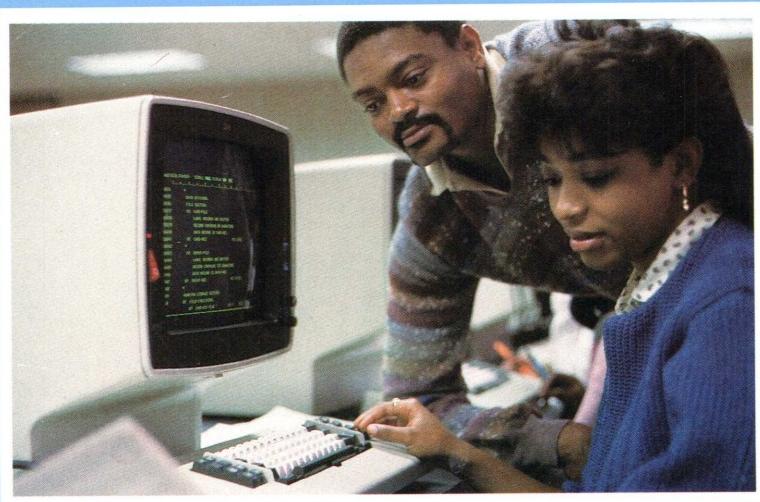
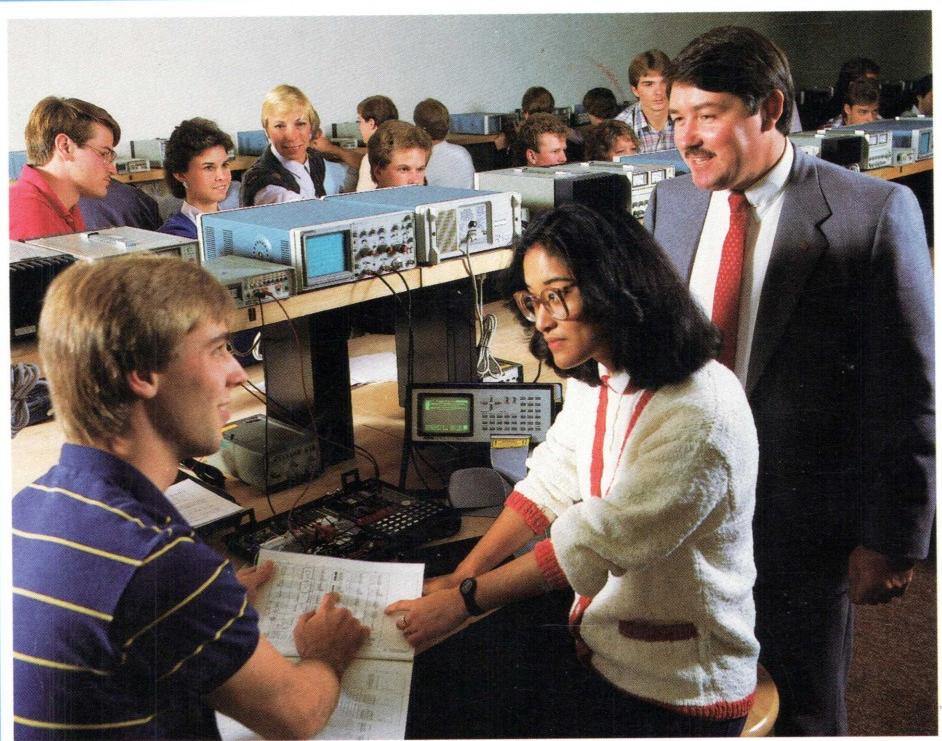
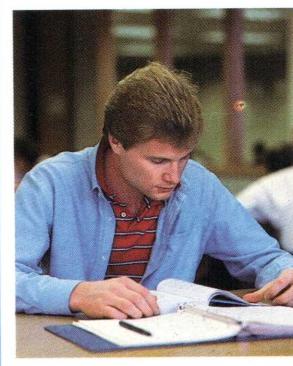
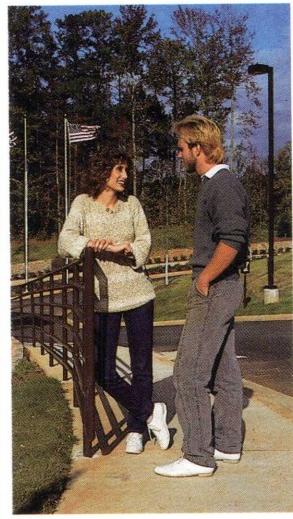


DeVRY

DeVry Institute of Technology Columbus, Ohio

1986 Academic Catalog Volume XXXIV





DeVry Inc., A Bell & Howell Company, includes a network of technical institutes with nine campuses in the U.S. and two in Canada specializing in Electronics Engineering Technology and Computer Information Systems. DeVry also provides training directly to business through its industrial training division. Bell & Howell Company provides quality products and services to customers in five business areas: career education, publishing, visual communication systems, information storage and retrieval, and document/mail processing.

DeVry Institute of Technology, Chicago, was established in 1931 by Dr. Herman A. DeVry, an inventor and motion picture projector manufacturer. In 1967, DeVry Institute of Technology in Chicago, Toronto and Phoenix joined the Bell & Howell Company.

The Institute in Columbus was established in 1952 as Ohio Technical College. Ohio Institute of Technology, Columbus, joined the Bell & Howell system in 1968. In May 1983, the name was changed to DeVry Institute of Technology.

In spring 1985, a Master of Project Management program was introduced at the DeVry Institute of Technology in Chicago.

The fall 1985 combined enrollment for the DeVry Institutes' day and night schools was over 29,000 students.

DeVry Institute of Technology A Bell & Howell Company

1350 Alum Creek Drive
Columbus, Ohio 43209-2764

Administrative Offices

(614) 253-7291
Open Monday through Friday 8 am to 5:30 pm

Admissions Office

(614) 253-1525
Open Monday through Thursday 8:30 am to 7:30 pm,
Friday 8:30 am to 5 pm and Saturday 9 am to 1 pm,
by appointment
Visitors are requested to phone in advance, if possible.

New Student Coordinator's Office

(614) 253-0851
(800) 426-3909 (within Ohio)
(800) 426-3916 (outside Ohio)
Open Monday through Friday 8:30 am to 5 pm

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Information, programs and requirements are subject to change without notice (at the discretion of the Administration). DeVry Inc. reserves the right to change the terms and conditions of this catalog at any time. This catalog supersedes all previous editions. January 1986. Photographs in this catalog are representative of DeVry Institutes system-wide.

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DeVry Institutes

Atlanta (Decatur), Georgia
Chicago, Illinois
Columbus, Ohio
Dallas (Irving), Texas
Kansas City, Missouri
Lombard, Illinois

Los Angeles (City of Industry), California
(Extension of DeVry, Phoenix)
Phoenix, Arizona
Woodbridge, New Jersey
Calgary, Alberta
Toronto (Weston), Ontario

**1985-1987
Academic Calendar**

1985 Fall Trimester	Monday Thursday-Sunday Saturday-Sunday Monday Monday Friday	October 28 November 28- December 1 December 21- January 5, 1986 January 6, 1986 January 20, 1986 February 21	Fall Trimester Begins Thanksgiving Recess Christmas Recess Classes Resume Martin Luther King, Jr. Day Fall Trimester Ends
1986 Spring Trimester	Monday Friday Monday Friday	March 3 March 28 May 26 June 13	Spring Trimester Begins Good Friday Memorial Day Spring Trimester Ends
1986 Summer Trimester	Monday Monday Friday	July 7 September 1 October 17	Summer Trimester Begins Labor Day Summer Trimester Ends
1986 Fall Trimester	Monday Thursday-Friday Friday-Monday Tuesday Monday Wednesday	October 27 November 27-28 December 19- January 5, 1987 January 6, 1987 January 19, 1987 February 25	Fall Trimester Begins Thanksgiving Recess Christmas Recess Classes Resume Martin Luther King, Jr. Day Fall Trimester Ends
1987 Spring Trimester	Monday Friday Monday Friday	March 9 April 17 May 25 June 19	Spring Trimester Begins Good Friday Memorial Day Spring Trimester Ends
1987 Summer Trimester	Monday Monday Friday	July 13 September 7 October 23	Summer Trimester Begins Labor Day Summer Trimester Ends
1987 Fall Trimester	Monday Thursday-Sunday Saturday-Sunday Monday Monday Friday	November 2 November 26-29 December 19- January 3, 1988 January 4, 1988 January 18, 1988 February 26	Fall Trimester Begins Thanksgiving Recess Christmas Recess Classes Resume Martin Luther King, Jr. Day Fall Trimester Ends

DeVry Institute of Technology prepares candidates for today's and tomorrow's technology

At DeVry, students concentrate right from the start on learning the skills that prepare them for a place in the expanding fields of technology. It is our intent to inform the student about the effect technology has had on the structure and dynamics of business and industry, and the various careers that can be pursued.

DeVry emphasizes the practical, teaching students theory along with the problem-solving skills necessary to zero-in on real life solutions.

For more information, interested persons are invited to contact the Director of Admissions or visit the Institute at their convenience.



The Mission and Goals of DeVry Institute of Technology

The mission of DeVry Institute of Technology is to provide career-oriented postsecondary programs of study in selected disciplines consistent with the current needs of business and industry and of new entrants into the labor force. In harmony with this mission, DeVry has established the following goals:

- The primary goal is to offer quality applications-oriented curricula. Curricula are designed to produce competent graduates who also have the ability to understand new developments in their field, adapt to change and grow throughout their careers. Consistent with this primary goal, DeVry seeks to enroll qualified students who can benefit from its programs.
- Another goal is to enhance students' success. To this end, faculty and academic staff will assist students in completion of their chosen programs. Outside of the classroom, related services are provided to assist students in achieving their educational objectives. DeVry is committed to a policy of nondiscrimination in admission, housing, financial aid and job placement with respect to sex, race, religion or national origin and will not refuse service to handicapped persons who are otherwise qualified.
- An additional goal is to assist graduates of full-time programs in achieving the highest possible degree of success in career-entry employment. Graduates are assisted in the search for positions compatible with their backgrounds and interests. Although the Institute cannot guarantee employment or salary levels, DeVry will continue to offer assistance following graduation.
- The goal of DeVry's continuing education and part-time programs is to enhance the opportunities for advancement and career mobility of individuals already employed or seeking a career change.
- A further goal of DeVry is to exercise fiscal responsibility in Institute operations. This is essential in maintaining quality education while keeping tuition rates at reasonable levels.
- The final goal is to achieve and maintain institutional and program accreditation with appropriate accrediting associations.

Accreditation

DeVry Institute of Technology, as an Institute of DeVry Inc., is accredited by the Commission on Institutions of Higher Education of the North Central Association of Colleges and Schools.

DeVry Institute of Technology is accredited by the Accrediting Commission of the National Association of Trade and Technical Schools (NATTS).

DeVry's Bachelor of Science Degree and Associate of Applied Science Degree programs in Electronics Engineering Technology are accredited by the Technology Accreditation Commission of the Accreditation Board for Engineering and Technology (TAC/ABET)*.

The purpose of ABET is the promotion and advancement of all phases of engineering education with a view to furthering the public welfare through the development of the better educated and qualified engineer, engineering technologist, engineering technician, and others engaged in engineering or engineering-related work.

North Central, NATTS and ABET are listed by the U.S. Department of Education as nationally recognized accrediting agencies. ABET is also recognized by the U.S. Department of Education as a nationally recognized accrediting agency for first professional degree programs in engineering, and associate and baccalaureate degree programs in engineering technology.

*The Institutes at Los Angeles and Lombard will apply when eligible.



Columbus, the Explorer's City

Students exploring Columbus will find that it caters to every taste. It is the 20th largest city in the United States, but it has never lost its small town atmosphere.

Columbus is a blend of leisurely living, educational centers, business, industry and culture. Columbus is also a world center of scientific and technological research and data dissemination.

Because it is a college town, Columbus is geared for young people. On five or six Saturdays every fall, 80,000 spectators squeeze into Ohio State University's giant Horseshoe Stadium to see Big Ten football at its best. In the summer, students boat on the Olentangy River and by Hoover Dam, picnic in the beautiful countryside outside the city, play tennis, swim and golf. Skiing and skating are popular in winter.

Time with friends is well spent in the Center of Science & Industry, the State Capitol area, the Gallery of Fine Arts, or as audience to the symphony orchestra, plays and major international entertainers.



The explorer finds the world's largest rose garden in Columbus, and a unique German village which is a restored section of the city as it appeared in the 1800s.

At night, nightclubs cater to every taste in music—jazz, rock, country-western or classical.





DeVry, Education in a Comfortable Environment

In 1952, Ohio Technical College opened its doors to students interested in radio and television servicing. In less than two decades, the school outgrew three facilities, achieved Associate and Bachelor's Degree-granting status, became one of the DeVry Institutes, and merited a change of title to Ohio Institute of Technology, to better reflect the quality of its programs.

In 1973, the Institute more than doubled in size by adding a 52,000 square foot structure to the original building.

The long, one-story steel structure is sheathed with anodized aluminum panels and silver reflective-glass windows. Interior areas may be closed off with sound-proof partitions to suit changing class and office needs.

A sunken commons area at the center of the building is the focus of the campus. Dining areas, a comfortable student lounge and meeting rooms for clubs encourage extracurricular activities.

In 1977, the Institute's 25th anniversary was observed. In July of 1980, the Institute expanded its curricula to include Computer Information Systems. In May 1983, the Institute was renamed DeVry Institute of Technology.



Student Activities

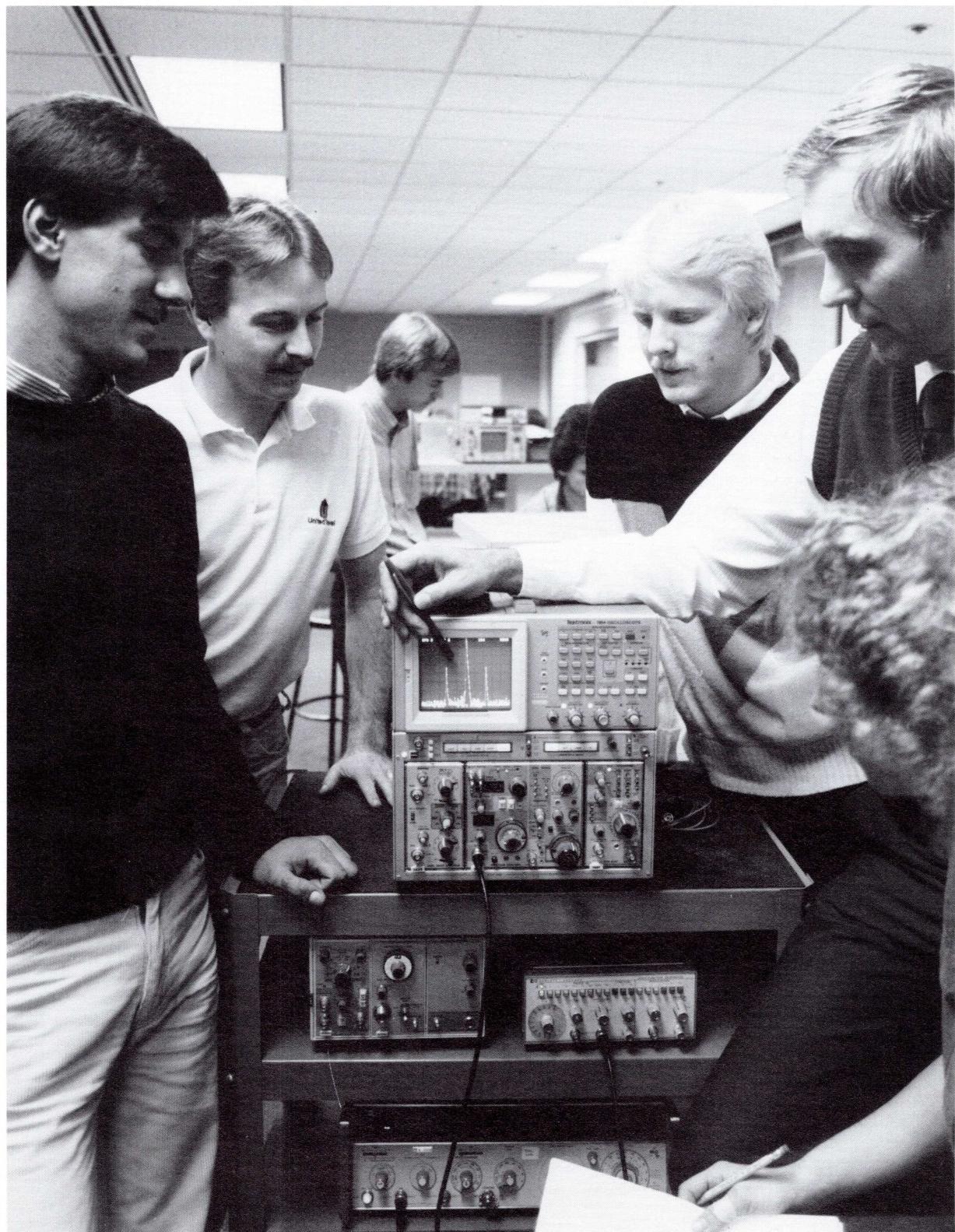
The Student Association plans activities ranging from concerts to lectures by professionals in high technology. There are monthly movies on campus, as well as picnics, raft trips, noontime shows and activities with students from other colleges.

DeVry has a full intramural sports program. Ping-pong, billiards and video game tournaments are held regularly.

Student organizations include the Institute of Electrical and Electronics Engineers (IEEE), the Data Processing Management Association (DPMA), the Stage Band, Tae Kwon Do, Christian Fellowship, Tau Alpha Pi, Black United Students (BUS), C.I.S. Student Board, and the Amateur Radio Club.

Clubs and activities reflect the interests of students and may change from time to time. Students who have interests not currently represented by a club are encouraged to start a new group.





Electronics as a Career Choice

Electronics technology, in which electronic systems and devices are used to equip society with improved products and services, is essential to our contemporary life.

Electronic equipment in operating rooms and hospital wards oversees life functions, communicating vital signs to health personnel. Electronic instruments help search oceans and fields for natural resources and solve energy problems. Electronic components are fundamental to computers—storing, processing and reissuing information for the business world.

Electronic components help control massive industrial machines, ships, automobiles, airplanes and towering structures. Electronics rules the entertainment field, converting a room of music, voice or drama to entertainment for the world. Electronic devices print and air the news, relay telephone calls, pace the heart and guide rocket probes.

Behind every electronic operation are the electronics technical specialists who have assisted in the design and are responsible for the installation, operation and maintenance of the electronic hardware. For the electronics professionals behind the circuits and signals, electronics is action—and action careers.

Through the programs offered by DeVry, graduates can look forward to specific electronics careers suitable to their educational backgrounds and fields of interest.

The graduate of the five-trimester **Electronics Technician Diploma** program is prepared for careers that emphasize the skilled maintenance and servicing of sophisticated electronic systems of many kinds, including communication systems, computers, controls and instrumentation. This includes troubleshooting to locate problems and then repairing, calibrating and adjusting the equipment. While this program is technically oriented, the broad knowledge base acquired by the graduates qualifies them for employment opportunities in related areas as well.

Examples of career-related areas would include, but are not limited to:

- Field Service Technician
- Electronic System Installation
- Electronic System Repair/Maintenance
- Circuit and System Prototyping
- Electronic Equipment/System Sales Support

Related areas for which graduates are also prepared include:

- Machine Tool Controller Repair/Maintenance
- Repair Service Supervision
- Electronic System Repair/Maintenance

The **Associate of Applied Science Degree in Electronics Engineering Technology** is earned after the completion of the first seven trimesters of the Bachelor's program. The graduate is prepared for work in such areas as field service, where it is possible to work closely with engineers and customers on all types of new electronic devices and systems. The graduate is generally involved in applying established engineering principles and concepts to design and development.

The graduate is prepared for careers in the following areas:

- Computer Technician
- Lab Technician
- Field Service Technician
- Electronic Equipment Repair/Maintenance
- Technical Writer
- Technical Associate

The graduate with a **Bachelor of Science Degree in Electronics Engineering Technology** (two additional trimesters of study) has acquired a capability for specialized work and for advancement to supervisory, managerial, and research and development positions. In the Bachelor's program, special emphasis is placed on the practical application of hardware systems and the theory behind their operation. This intensive technical background is elaborated upon by major laboratory projects which include planning, scheduling, procurement, construction and testing activities.

The graduate is prepared for careers in the following areas:

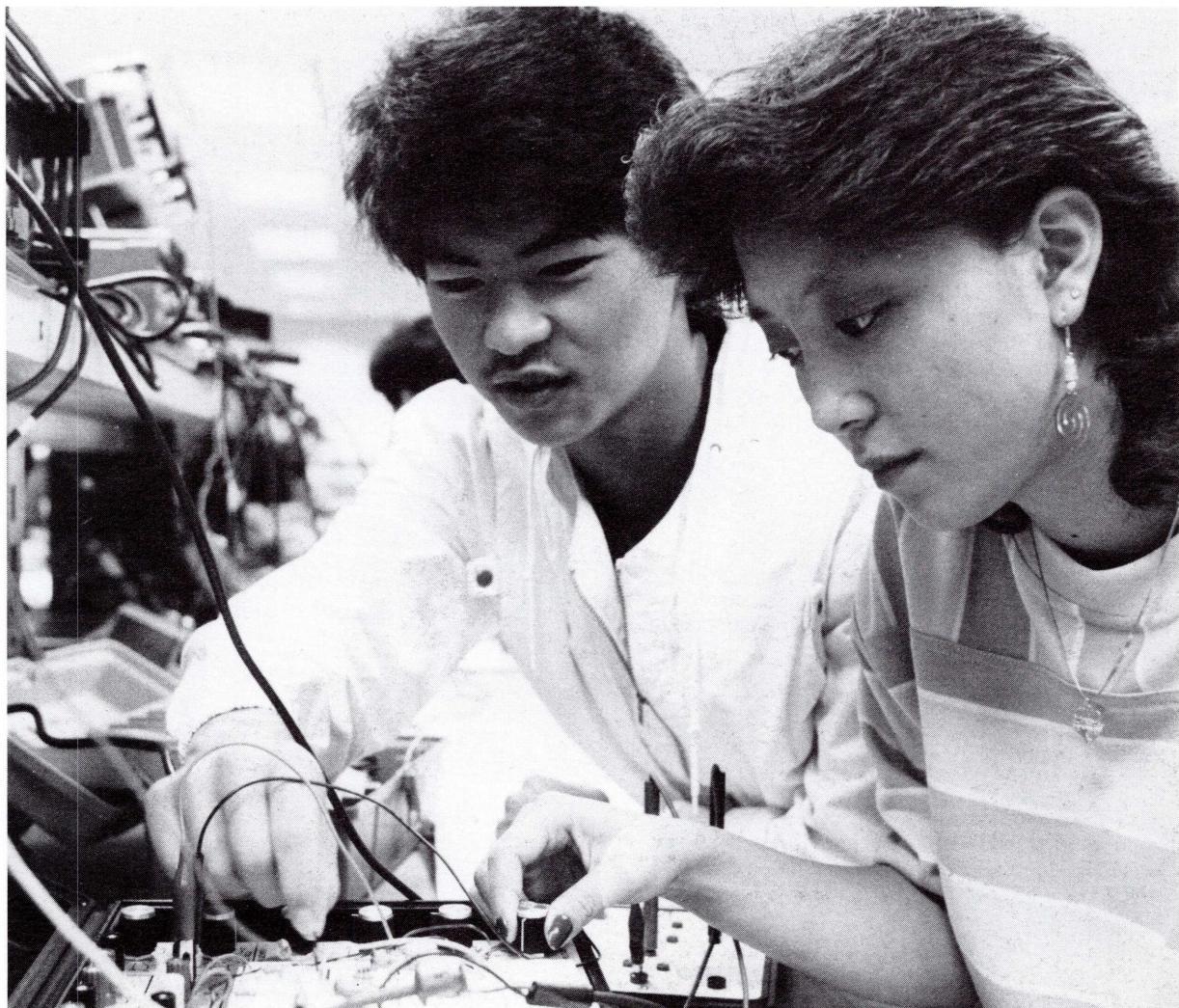
- Field Service Management
- Field Engineering Technologist
- Research and Development Technologist
- Technical Documentation
- Senior Technical Associate
- Product Development Technologist
- Quality Assurance Technologist
- Manufacturing Support

Related areas for which graduates are also prepared include:

- Entry-level Technical Management
- Small Business Management
- Technical Sales Support
- Technical Marketing Communications/Support

Evening school graduates earn a **Digital Electronics Technician Diploma** after six trimesters (part time) and work in the operation, maintenance, repair and troubleshooting of computers, instrumentation and control systems.

The starting levels of all graduates depend on both their prior work experience and the classifications used by the particular company for which they work. Typically, Electronics Engineering Technology graduates start at a higher level than Electronics Technician graduates, as illustrated by higher starting salaries. Additional details concerning these positions and compensation can be obtained from the Career Counseling and Placement Office.



Computer Information Systems as a Career Choice

The expansion of computers throughout business and industry continues to impact the flow of information resources. Computers perform new tasks, solve new problems and provide up-to-date information for making business decisions and controlling business organizations.

The progress of digital circuitry, including seemingly miraculous miniaturization, has resulted in computers of ever-decreasing size and weight and ever-increasing power and versatility.

Computers are at work in virtually all segments of our society, but the largest users are business firms. Such firms—large retail establishments, for example—use computers to facilitate such operations as billing, inventory control and payroll handling. Computers are also at work in other business and accounting functions in addition to providing the resources underlying information and decision support systems. Banks and investment houses, insurance companies, airlines, hotels and car rental firms plus a wide variety of small businesses are among the largest users of computers.

Because the computer has become a virtual business necessity, the Computer Information Systems curriculum being offered by DeVry is based upon systems technology and structured around a fundamental core of business, including business organization, accounting, economics and communications, and in the Bachelor's program, special systems technology and management studies.

While the Computer Information Systems program is technically oriented, the broad knowledge base acquired during the course of study qualifies the graduate for career opportunities in related areas as well.

The student who elects to graduate after six trimesters is awarded an **Associate of Applied Science Degree in Computer Information Systems**. The graduate is prepared for careers in the following areas:

- Entry-level Business Programmer
- Computer-related Sales Representative
- Software Support
- Computer-related Sales Support

The graduate with a **Bachelor of Science Degree in Computer Information Systems** (two additional trimesters) is prepared for advancement to positions of greater responsibility, including supervision and management.

Examples of career-related areas would include, but are not limited to:

- Business Applications Programming
- Computer-related Sales
- Data Base Applications
- System Software Support
- Controls and Security
- Field Hardware/Software Support

Related areas for which graduates are also prepared include:

- Entry-level Management
- Small Business Management
- Sales and Marketing Support
- Business and Accounting



Program Information

DeVry Institute of Technology offers students a choice of the following curricula:

Electronics

Electronics Technician Diploma program—
5 trimesters

Associate of Applied Science Degree in Electronics
Engineering Technology—7 trimesters*

Bachelor of Science Degree in Electronics
Engineering Technology—9 trimesters*

*After successfully completing the first seven trimesters of the Bachelor's program, the student is eligible for the Associate Degree. The Bachelor's Degree is earned after the completion of two additional trimesters and all requirements for graduation are fulfilled. The Associate Degree and Bachelor's Degree programs are accredited by the Technology Accreditation Commission of the Accreditation Board for Engineering and Technology. (The Los Angeles and Lombard Institutes will apply when eligible.)

Computer Information Systems

Associate of Applied Science Degree in
Computer Information Systems—6 trimesters*

Bachelor of Science Degree in Computer
Information Systems—8 trimesters*

*After successfully completing the first six trimesters of the Bachelor's program, the student is eligible for the Associate Degree. The Bachelor's Degree is earned after the completion of two additional trimesters and all requirements for graduation are fulfilled.

Evening School

Digital Electronics Technician Diploma program—
6 trimesters (part time only)

General Information

Each trimester consists of 15 weeks of instruction. Each week, between 19 and 21 hours of class and lab work are required in the Electronics Technician program. Approximately 18-22 hours of work per week are required in each trimester in both the Electronics Engineering Technology and in the Computer Information Systems programs.

Students enrolled in the part-time evening school program attend three nights a week with approximately 12 hours of class and lab work per week.

Before selecting any of the above programs, applicants should determine their career aspirations. Students are encouraged to look at their backgrounds, aptitudes and interests before making a career decision. At DeVry, we help students resolve these questions before their studies begin. Experienced faculty and counselors advise students on selecting the programs and careers that best suit their capabilities.



Electronics Day Programs

Graduation Requirements

The actual sequence in which some courses are taken may vary. The arrangement shown is typical. Credit hours listed are semester hours as defined by the American Association of Collegiate Registrars and Admissions Officers. Contact hours relate to time spent in class.

DeVry Inc. reserves the right to alter the total number of contact hours listed below for reasons including, but not limited to, the following: natural occurrences beyond DeVry's control, holidays, special institutional activity days and registration days.

Electronics Engineering Technology Curriculum

	Contact Hours Per Week	Credit Hours
First Trimester		
Circuit Analysis I (CA-103)	6	6
College Algebra (A-102)	4	4
Structured BASIC Programming (BP-101)	4	4
Student Orientation (SO-102)	1	0
Circuit Analysis I Laboratory (CA-103L)	2	1
Structured BASIC Programming Laboratory (BP-101L)	2	1
	19	16

Second Trimester

Circuit Analysis II (CA-203)	4	4
Electronic Devices and Circuits I (ED-203)	4	4
Trigonometry (T-201)	5	5
Psychology (PSY-201)	3	3
Circuit Analysis II Laboratory (CA-203L)	2	1
Electronic Devices and Circuits I Laboratory (ED-203L)	2	1
	20	18

Third Trimester

Electronic Devices and Circuits II (ED-302)	4	4
Digital Circuits (DC-302)	4	4
Calculus I (C-302)	5	5
English (E-301)	3	3
Electronic Devices and Circuits II Laboratory (ED-302L)	2	1
Digital Circuits Laboratory (DC-302L)	2	1
	20	18

Contact Hours Per Week	Credit Hours
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Fourth Trimester

Electronic Devices and Circuits III (ED-402)	4	4
Digital Systems (DS-401)	4	4
Calculus II (C-402)	5	5
Technical Writing (TW-401)	3	3
Electronic Devices and Circuits III Laboratory (ED-402L)	2	1
Digital Systems Laboratory (DS-401L)	2	1
	20	18

Fifth Trimester

Transform Analysis and Applications (TA-501)	4	4
Microprocessor Hardware and Software (MS-501)	4	4
Physics I (P-502)	4	4
Economics (EC-502)	3	3
Microprocessor Hardware and Software Laboratory (MS-501L)	2	1
Physics I Laboratory (P-502L)	2	1
Transform Analysis and Applications Laboratory (TA-501L)	2	1
	21	18

Sixth Trimester

Communication Circuits and Systems (CS-601)	5	5
Microprocessor Peripherals (MP-601)	4	4
Public Speaking (PS-603)	4	3
Physics II (P-602)	3	3
Communication Circuits and Systems Laboratory (CS-601L)	2	1
Microprocessor Peripherals Laboratory (MP-601L)	2	1
Physics II Laboratory (P-602L)	2	1
	22	18

Seventh Trimester

Digital Communications (DC-701)	5	5
Control Systems I (CT-702)	4	4
Principles of Management (PM-701)	3	3
Pascal Programming (CP-703)	3	3
Digital Communications Laboratory (DC-701L)	2	1
Control Systems I Laboratory (CT-702L)	2	1
Pascal Programming Laboratory (CP-703L)	2	1
	21	18

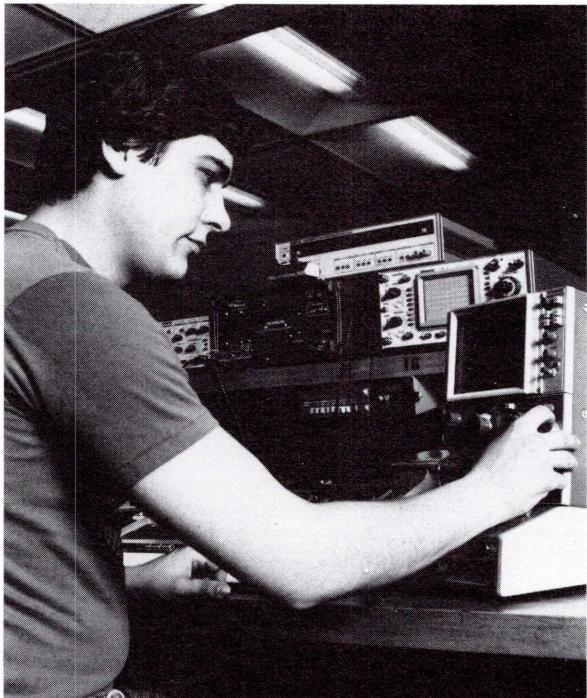
TOTAL HOURS FOR THE SEVEN-TRIMESTER PROGRAM

143 124

For the full seven-trimester (105 week) Electronics Engineering Technology program, the total number of contact hours is 2145.

Students are eligible for an Associate of Applied Science Degree upon satisfactory completion of these seven trimesters. After successful completion of an additional two trimesters and all graduation requirements, students are eligible for the Bachelor of Science Degree. Both degree programs are accredited by the Technology Accreditation Commission of the Accreditation Board of Engineering and Technology. (The Institutes at Los Angeles and Lombard will apply when eligible.)

Eighth Trimester	Contact Hours Per Week	Credit Hours
Control Systems II (CT-802)	4	4
Microprocessor Systems I (MS-801)	3	3
Contemporary History (CH-801)	3	3
Contemporary Literature (CL-801)	3	3
Career Development (CD-801)	2	2
Control Systems II Laboratory (CT-802L)	2	1
Microprocessor Systems I Laboratory (MS-801L)	2	1
	19	17



Ninth Trimester	Contact Hours Per Week	Credit Hours
Advanced Communication Systems (CS-901)	5	5
Microprocessor Systems II (MS-901)	3	3
Social Issues in Technology (ST-901)	3	3
Business Operations (OP-901)	3	3
Microprocessor Systems II Laboratory (MS-901L)	2	1
Advanced Communication Systems Laboratory (CS-901L)	2	1
	18	16

TOTAL HOURS FOR THE NINE-TRIMESTER PROGRAM 180 157

For the full nine-trimester (135 week) Electronics Engineering Technology program, the total number of contact hours is 2700.



Electronics Technician Curriculum

	Contact Hours Per Week	Credit Hours		Contact Hours Per Week	Credit Hours
First Trimester			Fifth Trimester		
Technical Mathematics I (TM-10)	3	3	Technical Communication (TN-50)	3	3
Supplemental Algebra* (SA-11)	3*	0	Microprocessor Applications (MA-50)	5	5
Electronics Fundamentals (EF-11)	6	6	Advanced Communication Systems (CS-50)	4	4
Digital I (DI-10)	3	3	Advanced Industrial Control Systems (CT-50)	3	3
Student Orientation (SO-12)	1	0	Advanced Industrial Control Systems Laboratory (CT-50L)	2	1
Electronics Fundamentals Laboratory (EF-11L)	4	2	Microprocessor Applications Laboratory (MA-50L)	2	1
Digital I Laboratory (DI-10L)	2	1	Advanced Communication Systems Laboratory (CS-50L)	2	1
	19	15			
Second Trimester					
Technical Mathematics II (TM-20)	3	3			
Electronics Applications (EA-20)	4	4			
Electronic Devices (ED-20)	4	4			
Digital II (DI-20)	3	3			
Digital II Laboratory (DI-20L)	2	1			
Electronics Applications Laboratory (EA-20L)	2	1			
Electronic Devices Laboratory (ED-20L)	2	1			
	20	17			
Third Trimester			TOTAL HOURS FOR THE FIVE-TRIMESTER PROGRAM	102	86
Communication Skills (CS-32)	3	3			
Introduction to Microprocessors (MP-30)	5	5			
Basic Electronic Communications (BC-30)	4	4			
Industrial Control Systems (CT-30)	3	3			
Industrial Control Systems Laboratory (CT-30L)	2	1			
Introduction to Microprocessors Laboratory (MP-30L)	2	1			
Basic Electronic Communications Laboratory (BC-30L)	2	1			
	21	18			
Fourth Trimester					
Career Development (CD-42)	3	3			
Microprocessor Systems (MS-40)	5	5			
Analog Communication Systems (AC-40)	3	3			
Digital/Data Communication Systems (DD-40)	4	4			
Digital/Data Communication Systems Laboratory (DD-40L)	2	1			
Microprocessor Systems Laboratory (MS-40L)	2	1			
Analog Communication Systems Laboratory (AC-40L)	2	1			
	21	18			

For the full five-trimester (75 week) Electronics Technician program, the total number of contact hours is 1530.

Electronics Technician credits are not applicable towards the Electronics Engineering Technology curriculum.

*Students who score less than 72% on the algebra Entrance Examination and who wish to enter the Electronics Engineering Technology program enroll in the first trimester of the Electronics Technician program and attend additional hours of algebra classes each week. Upon successful completion of the first trimester and successful retest of the algebra Entrance Examination, students may enter the first trimester of the Electronics Engineering Technology program.

Computer Information Systems Program Graduation Requirements

The actual sequence in which some courses are taken may vary. The arrangement shown is typical. Credit hours listed are semester hours as defined by the American Association of Collegiate Registrars and Admissions Officers.

DeVry Inc. reserves the right to alter the total number of contact hours listed below for reasons including, but not limited to, the following: natural occurrences beyond DeVry's control, holidays, special institutional activity days and registration days.

Computer Information Systems Curriculum

First Trimester	Contact Hours Per Week	Credit Hours
Introduction to Data Processing (IS-100)	3	3
Pascal Programming (CP-100)	5	5
Applied Algebra (DM-100)	4	4
Business Organization (CSS-100)	3	3
Pascal Laboratory (CP-100L)	4	2
Student Orientation (SO-102)	1	0
	20	17

	Contact Hours Per Week	Credit Hours
Second Trimester		
COBOL I (CP-110)	4	4
Data Processing Math (DM-110)	4	4
Introduction to Accounting (AC-100)	4	4
English I (CCS-100)	3	3
COBOL Laboratory I (CP-110L)	4	2
	19	17

Third Trimester

COBOL II (CP-220)	4	4
Systems Analysis I (SY-200)	4	4
Financial Accounting (AC-210)	4	4
English II (CCS-210)	3	3
COBOL Laboratory II (CP-220L)	4	2
	19	17

Fourth Trimester

Systems Analysis II (SY-210)	4	4
Operating Systems and JCL (IS-210)	4	4
Introduction to Statistics (DM-220)	3	3
Business Writing (CCS-220)	3	3
Psychology (CSS-210)	3	3
JCL Laboratory (IS-210L)	2	1
	19	18



	Contact Hours Per Week	Credit Hours		Contact Hours Per Week	Credit Hours
Fifth Trimester					
BAL Programming (CP-330)	4	4	Data Structures (SY-420)	4	4
Small Business Computer Systems (IS-320)	4	4	On-Line Systems (IS-440)	4	4
Public Speaking (CCS-330)	4	3	Management Information Systems (IS-450)	3	3
Economics (CSS-320)	4	4	Career Development (CSS-440)	2	2
BAL Laboratory (CP-330L)	4	2	Contemporary Literature (CLA-400)	3	3
	20	17	Data Structures Laboratory (SY-420L)	2	1
			On-Line Systems Laboratory (IS-440L)	2	1
				20	18
Sixth Trimester					
Applications/Maintenance Programming (CP-340)	4	4	Senior Project (SY-430)	3	3
Controls & Security (IS-330)	4	4	Distributed Data Processing (IS-460)	4	4
Managerial Accounting (AC-320)	4	4	Data Base Management Systems (IS-470)	5	5
Management (CSS-330)	3	3	Perspectives on Technology (CLA-410)	4	4
Applications/Maintenance Programming Laboratory (CP-340L)	2	1	Data Base Management Systems Laboratory (IS-470L)	4	2
Controls & Security Laboratory (IS-330L)	2	1		20	18
	19	17			
TOTAL HOURS FOR THE SIX-TRIMESTER PROGRAM	116	103	TOTAL HOURS FOR THE EIGHT-TRIMESTER PROGRAM	156	139

For the full six-trimester (90 week) Computer Information Systems program, the total number of contact hours is 1740.

Students are eligible for an Associate of Applied Science Degree after satisfactory completion of six trimesters.

Seventh Trimester

Data Structures (SY-420)	4	4
On-Line Systems (IS-440)	4	4
Management Information Systems (IS-450)	3	3
Career Development (CSS-440)	2	2
Contemporary Literature (CLA-400)	3	3
Data Structures Laboratory (SY-420L)	2	1
On-Line Systems Laboratory (IS-440L)	2	1

Eighth Trimester

Senior Project (SY-430)	3	3
Distributed Data Processing (IS-460)	4	4
Data Base Management Systems (IS-470)	5	5
Perspectives on Technology (CLA-410)	4	4
Data Base Management Systems Laboratory (IS-470L)	4	2

TOTAL HOURS FOR THE EIGHT-TRIMESTER PROGRAM

For the full eight-trimester (120 week) Computer Information Systems program, the total number of contact hours is 2340.

Students are eligible for a Bachelor of Science Degree after satisfactory completion of eight trimesters and all graduation requirements.



Evening Program Graduation Requirements

The actual sequence in which some courses are taken may vary. The arrangement shown is typical. Credit hours listed are semester hours as defined by the American Association of Collegiate Registrars and Admissions Officers.

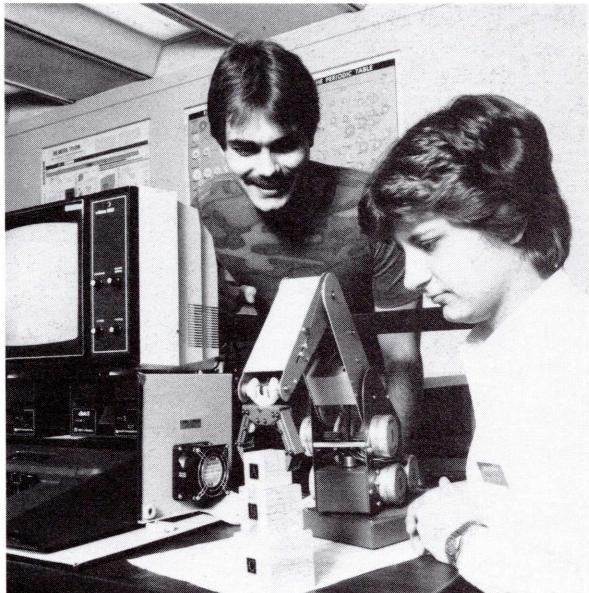
DeVry Inc. reserves the right to alter the total number of contact hours listed below for reasons including, but not limited to, the following: natural occurrences beyond DeVry's control, holidays, special institutional activity days and registration days.

Digital Electronics Technician Curriculum

	Contact Hours	Credit Hours
	Per Week	

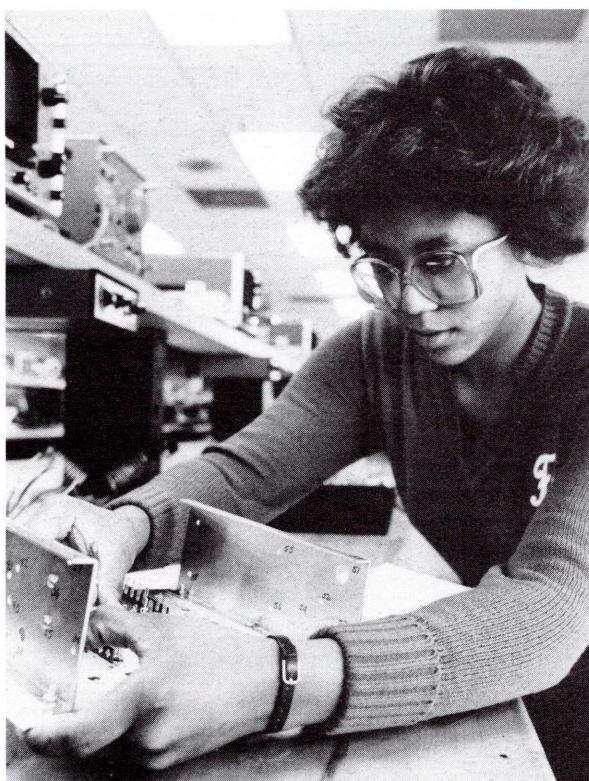
First Trimester

Electronics Fundamentals I (EFN-12)	8	8
Electronics Laboratory I (ELN-12)	4	2
	12	10



Second Trimester

Electronics Fundamentals II (EFN-22)	8	8
Electronics Laboratory II (ELN-22)	4	2
	12	10



Third Trimester

Electronics Fundamentals III (EFN-32)	8	8
Electronics Laboratory III (ELN-32)	4	2
	12	10

Fourth Trimester

Linear and Digital Circuits (LDN-42)	8	8
Electronics Laboratory IV (ELN-42)	4	2
	12	10

Fifth Trimester

Digital Computers (DCN-52)	8	8
Electronics Laboratory V (ELN-52)	4	2
	12	10

Sixth Trimester

Microcomputer Systems (MSN-62)	8	8
Electronics Laboratory VI (ELN-62)	4	2
	12	10

**TOTAL HOURS FOR THE SIX-TRIMESTER
PROGRAM** 72 60

For the full six-trimester (90 week) Digital Electronics Technician program, the total number of contact hours is 1080.

Admission and Academic Information

Admission Policy-General

All students must be at least 17 years of age on or before the first day of classes. The basic admission requirement for all programs is high school graduation or possession of a General Education Development Certificate (GED). Proof of high school graduation or equivalency must be provided for the student's file by the end of registration unless the Institute grants an extension. Applicants with prior postsecondary credit must present transcripts indicating all previous work. All students must also pass Entrance Examinations or submit acceptable ACT/SAT/WPCT scores. (See chart below for details.) Applicants who do not receive a satisfactory grade on one of these exams will be denied admission and will receive a refund of their application fee and prepaid tuition/registration fee. Applications for a forming class may be taken through late registration.

Admission Procedure

Prospective students are welcome to visit the Institute and make application with the Admissions Office or with a DeVry Inc. field representative. A representative of DeVry Inc. in the student's geographical area can be contacted to arrange an interview with a prospective student and can provide full information about courses, class starting dates, student housing, part-time work and employment opportunities for graduates. Interested students may contact the Institute for a personal conference with a representative.

When all application requirements have been fulfilled, applicants will be notified promptly regarding their admission status.

Registration and orientation take place during the week or weekend before classes begin as scheduled by the Institute.

Admission Requirements and Examinations for Electronics and Computer Programs

The applicant must meet the Entrance Examination requirements or one of the standard test requirements to be eligible for admission into the Institute.

Entrance Examinations: Minimum Scores Required

	DeVry Entrance Examinations			or	Standard Tests
	Arithmetic	Algebra	Logic	SAT/WPCT	ACT
Electronics Engineering Technology	72%	72%*	Not Applicable	480 (Math)	23 (Math)
Electronics Technician	52%**	Not Applicable	Not Applicable	480 (Math)	23 (Math)
Digital Electronics Technician	72%	Not Applicable	Not Applicable	480 (Math)	23 (Math)
Computer Information Systems	72%	Not Applicable	65%	840 (Composite)	20 (Composite)

*Students who score less than 72% on the algebra Entrance Examination and who wish to enter the Electronics Engineering Technology program enroll in the first trimester of the Electronics Technician program and attend additional hours of algebra classes each week. To enter the Electronics Engineering Technology program, a student must successfully complete the additional hours of algebra, pass all courses in the first term of the Electronics Technician program, achieve a term grade

point average of 2.00 and score 72% or higher on an alternate form of the algebra Entrance Examination. The additional term required will increase the applicant's program of study by one trimester and total tuition cost of the program by \$1825, based on present rates.

**Students who score less than 72% correct on the arithmetic portion of the Entrance Examination but 52% or greater will be admitted, but enrollment of these students is limited, and on a first-come, first-served basis.

It must be noted that applicants occasionally fail to pass the Entrance Examinations the first time. In such instances, applicants will be given the option to retest or receive a refund of the tuition deposit/registration fee and application fee.

If the applicant's first-time scores do not meet the minimum scores described in the chart on page 20, then one retest of each examination will be permitted. The qualifying arithmetic and algebra scores are the same as those indicated in the table. The qualifying logical reasoning retest score is 70%. A second retest in math will be permitted only under special circumstances approved by the Dean of Academic Affairs. There is no second retest for logical reasoning.

Applicants for the electronics programs should note that due to the use of color as one method of coding electronic components, individuals who are color-blind may have difficulty with portions of the course work in the electronics programs.

Important Facts on Your Education

At the time of application, prospective students receive a "Personal File" package which contains an "Important Facts on Your Education" folder to ensure their understanding of academic programs, student life and services. The representative will review this important material, which covers academic information, financial information, student financing, student services and graduate placement. If any questions exist after these documents are read, applicants should mail the enclosed postage-paid card or call their representative to have questions answered or problems resolved by school officials.

Pre-Registration Activities Program

Pre-Registration Activities (PRA) have been developed by DeVry to help entering students prepare for registration before their actual class starting date. The acronym PRA also stands for the goal of these activities, that is to PREPARE, REINFORCE and ANSWER. The PRA sessions prevent the last minute piling up of pre-registration requirements, details and paperwork that would otherwise interfere with a smooth start for beginning students. They also provide a good opportunity for the family to learn about careers in technology and to become acquainted with the DeVry Institutes and their services.

At the time of application, students indicate when and where they will take care of their Pre-Registration Activities. Schedules and locations for off-campus events are arranged so that most students can, in a one-day trip, travel to the event, take care of the required activities and return home. Events are held at the DeVry Institutes and at a variety of locations around the country. Events are usually scheduled on weekends or evenings for maximum convenience. At all PRA sessions students can take the Entrance Examinations and, at on-campus PRAs, receive assistance in completing financial aid paperwork, among other services.

Students who live less than three hours driving time from the Institute are expected to visit the campus during one of our weekend PRA sessions or schedule an appointment any weekday between 9 a.m. and 4 p.m. Those students living more than three hours driving time are, of course, welcome to attend campus events, but may prefer to attend an off-campus event.

For noncommuting students, special arrangements may be made with the New Student Coordinator to accommodate students who are legitimately unable to attend a PRA event or visit the Institute on a weekday. However, the value of attending a PRA event cannot be overstated since it allows students to fulfill all pre-registration obligations and to learn firsthand about the Institute's programs and services.

Representatives will assist students in selecting the most convenient PRA session to attend. Also, assistance will be provided to complete the PRA reservation cards and answer questions concerning the programs. Prior to the scheduled PRA event students will receive a reminder giving them instructions on what to bring and how to prepare for the program.

General

Should a situation arise in which more applicants than can be accommodated arrive at the Institute to start classes, those who cannot start will be given the following choices: (1) a transfer to another DeVry Institute which offers the program, in which case DeVry Inc. will pay the applicant's transportation costs, most economical as selected by DeVry and for only that first trimester, to the other Institute; (2) a refund, within 30 days, of the tuition deposit (registration fee in Illinois) and application fee paid. Cost of round-trip transportation in both instances, most economical as selected by DeVry, from the applicant's home to the Institute will also be paid for only that first trimester.

Admission of International Students

(Except DeVry, Los Angeles)*

The Immigration and Naturalization Service of the U.S. Government, as well as DeVry Inc., require certain financial and academic documentation prior to issuing the Immigration Form I-20 for admission. International applicants must provide the Institute with the following items before an applicant can be accepted for admission:

- a certified copy of their high school transcript or equivalent (translated into English by a certified translator);
- a notarized statement of financial support explaining that tuition will be paid in advance of each term and that a sponsor will provide all necessary living expenses for the international applicant (Form I-134 may be used). Applicants should also be aware that they will not be eligible for financial assistance and cannot work legally in the United States without permission from the U.S. Immigration and Naturalization Service;
- proof of English language proficiency (see Nonnative Speakers of English section for details);
- must meet DeVry entrance requirements (see Admission Requirements section).

After the above information is received, the applicant's acceptance status will be determined. If all documents are in order, a Form I-20 will be issued. The Form I-20 will enable the international applicant to obtain an F-1 Class visa, or in some instances an M-1 Class visa, to enter the United States. For international students transferring from one educational institution in the United States to another, applicants must have been enrolled in that present institution for at least one academic term before their application to DeVry will be considered. Students who presently have an F-1 (or M-1) Class visa and are remaining in the same educational program at the same level only need to obtain a Form I-20A-B (or M-N) from DeVry. However, all other transfer students must also obtain a Form I-538 from their present educational institution.

In addition, if the student is not already in the U.S., a health history form is required by the U.S. Immigration and Naturalization Service.

*DeVry, Los Angeles is in the process of applying for certification by the Immigration and Naturalization Service of the U.S. Government. Once certified, the Institute will be able to accept international students. The above policy will be in effect when the Institute becomes certified. For current information on the status of the Institute's certification, contact the Director of Admissions.

Admission of Nonnative Speakers of English

Proof of English language proficiency is required for applicants who graduated from high schools outside the U.S. This must be demonstrated by providing the Institute with one of the following:

- evidence of a score of 500 or higher on the TOEFL test (Test of English as a Foreign Language) or;
- a certificate of completion of an intermediate ESL (English as a Second Language) course.

Applicants may be exempt from the above requirements by meeting one of the following criteria:

- applicants from approved English-speaking countries;
- applicants from countries not approved but who graduated from high schools or approved GED programs in which the language of instruction was English. Acceptable documentation must be submitted to the Institute and approved by the Dean of Academic Affairs.
- students who have completed two academic terms, with at least a C average, at a postsecondary institution in which the language of instruction was English. Acceptable documentation must be submitted to the Institute and approved by the Dean of Academic Affairs.

For details regarding this policy, applicants should contact the Director of Admissions at the Institute. The Director of Admissions will also provide the necessary instructions for those who need to take the TOEFL examination.

Grading Policy

Letter grades for each subject are assigned at the end of each trimester as follows:

Grade	Percentage Equivalent	Grade Point Index
A	90-100	4
B	80-89	3
C	70-79	2
D	60-69	1
F	Below 60	0
I	Incomplete	0
K	External Transfer Credit	
P	Proficiency Exam	
S	Satisfactory (noncredit course only)	
T	Internal Transfer Credit	
V	Course Audit	
W	Withdrawal	

The grade points earned in each course are computed by multiplying the number of credit hours for the course by the grade point index (from the table) for the final grade achieved. The Trimester Grade Point Average (TGPA) is computed by adding the grade points earned in all courses, and dividing this sum by the total number of credit hours for the trimester. The Cumulative Grade Point Average (CGPA) is the total grade points divided by the total credit hours for all trimesters to date. Both GPAs are based only on grade points earned in the present program of enrollment.

A student whose TGPA is between 3.00 and 3.49 is recognized by being named to the Dean's List. A TGPA between 3.50 and 4.00 qualifies the student for the President's List. A CGPA between 3.50 and 4.00 after two consecutive terms merits membership in the Presidential Honor Society. A grade of F in any course makes a student ineligible for any honors until the course is successfully completed with a passing grade.

A grade of I (Incomplete) signifies that the student has not completed all required course work. The grade of I must be converted to a final letter grade by completion of course work within 15 class days after the end of the term in which the course was taken. Failure to complete the course work within 15 class days will result in the I being converted to a grade of F. Incomplete grades will be issued only under highly unusual circumstances and require prior satisfactory academic progress.

A grade of W indicates the student withdrew from that course prior to the end of the 11th week.

A grade of F in any course denotes failure. Any student who receives an F must repeat the course with a passing grade or apply transfer credit for the course in order to be eligible to graduate. Courses for which the grade of F is awarded are included in the student's GPA, regardless of the reason for the F, until the student repeats the course with a passing grade.

The letter K is used if transfer credit is allowed for a non-DeVry course. Courses for which transfer credit is allowed are not included in the determination of the GPA.

A grade of P is used if the student demonstrates proficiency in the course. The letter V is used to indicate an audited course. The letter T is used if the student has transferred credit for a course from another program within the DeVry system. Grades of P, V and T are not included in the GPA.

The grade of S is used to indicate satisfactory completion of a noncredit course.

Satisfactory Academic Progress

DeVry Institute of Technology expects students to demonstrate satisfactory academic progress toward completion of their curriculum of study. Satisfactory academic progress is defined as movement from one academic level to the next. Satisfactory academic progress affects student financial aid eligibility status.

Academic Standing

To be in good academic standing, a student must have a CGPA of 2.00 or greater and maintain satisfactory academic progress. If at the end of an academic term a student's CGPA is less than 2.00, the student will be placed on academic probation. Academic probation is defined as a period of usually one trimester in which students must improve their academic achievement to avoid being suspended from the Institute. If at the end of a probationary term: a) the CGPA has been raised to at least 2.00, the student will be returned to good academic standing; b) the student's TGPA is above 2.00, and the CGPA is still below 2.00, the student will remain on probation for one additional term; c) the student's TGPA and CGPA are both below 2.00, the student will be placed on academic suspension.

Academic Suspension

Academic suspension is defined as officially interrupting a student from the Institute as a result of academic deficiencies. Academic suspension will occur if: a) the student on academic suspension does not appeal; or b) if an academic appeal is denied by the Academic Dean of the curriculum. Probation and suspension actions will be recorded on the student's permanent academic transcript.

Academic Appeal Process

A student who is placed on academic suspension may appeal. Refer to the Student Handbook for details.

Academic Suspension Reinstatement/ Readmission Conditions

A student who is reinstated based on the results of an academic appeal or readmitted based on the results of an academic readmission application will continue on probation and remain subject to the Academic Standing provision. If after the completion of the first trimester following reinstatement the CGPA is not raised to at least 2.00, a second suspension will occur, the student will then be expelled, and readmission will not normally occur. A student who has been suspended for academic deficiencies may apply for readmission by submitting a written request for readmission to the Academic Dean

after one trimester of nonattendance. If a request for readmission is not completed within three trimesters after suspension, reapplication must be made with the Director of Admissions.

Academic Expulsion/Readmission

Academic expulsion results from failure to meet the academic conditions of the first trimester following reinstatement. Academic expulsion usually is permanent and terminates student status for an indefinite period of time. The conditions for readmission, if any, will be stated in writing by the Academic Dean responsible for the expulsion action.

Resumption of Study

Students seeking resumption of study after expulsion or voluntary withdrawal, need to be aware that not all courses are offered each term, and that curriculum changes may have occurred. Students who have voluntarily withdrawn may return at the beginning of any term. A review with the Academic Dean will be made to determine the modifications necessary to allow those students to complete the graduation requirements.

Students who have voluntarily withdrawn from school more than once may only be readmitted by permission of the appropriate Academic Dean.

Any students returning to DeVry after withdrawing from school will be required to execute a new enrollment agreement/contract prior to resuming.

Transfer of Curriculum During Probation

A student on probation in one curriculum who transfers to another curriculum within the Institute will retain probationary status after the transfer. If upon completion of the probationary trimester in the new curriculum the student's TGPA is less than 2.00, academic suspension will occur. In addition, if a student who is enrolled in one curriculum transfers to a second curriculum, and then returns to the original curriculum of enrollment, his/her academic status will be based upon the academic performance for all terms completed in the original curriculum.

Voluntary Repeat of Courses

When any course is taken for a second time, the record of the second attempt will be added to the student's transcript, but the record of the first attempt will also remain. The higher of the two grades will be used in GPA computations, dropping the lowest grade completely.

Academic Year Progress Status

DeVry defines an academic year as a minimum of two trimesters, and satisfactory academic progress as movement from one academic level to the next. In order to progress in academic level, a student must complete the required number of credit hours within two trimesters and earn credit hours according to the appropriate table below. (Note: earned credit hours result from grades of A, B, C, D, P, T and K.)

Academic Level Status

(Day Programs)

Electronics Technician (Diploma)
Electronics Engineering Technology (Associate,
Bachelor's Degree)
Computer Information Systems (Associate,
Bachelor's Degree)

Academic Level	Earned Credit Hours
1	Less than 26
2	26 but less than 52
3	52 but less than 78
4	78 but less than 105
5	105 and higher

(Evening Program)

Digital Electronics Technician (Diploma)

Academic Level	Earned Credit Hours
1	Less than 15
2	15 but less than 30
3	30 but less than 45
4	45 but less than or equal to 60

Multiple Course Repeats

Satisfactory academic progress is supported by the structure of DeVry curricula, and limitations are placed on multiple repeats of a course. Students may not enroll in a course more than twice except under extenuating circumstances and with the special written permission from the Academic Dean of the curriculum of enrollment.

Audited Courses

A student may audit a course with the permission of the appropriate Academic Dean. Normal tuition rates apply to audited courses. Financial aid cannot be applied to audited courses.

Proficiency Credit

A student who feels the material in a course is already mastered, either through courses taken at another school for which transfer credit cannot be given or through self-study, may request a proficiency examination for the course provided the student was not previously enrolled in the course at a DeVry Institute. The request must be made no later than the close of registration.

Transfer of Credit

Transfer credit will be given for all college-level courses taken at an accredited, postsecondary institution provided a grade of C or better was earned and the course was comparable to a DeVry course. When a student receives such transfer credit, the course is excluded from the CGPA computation. Such applicants must arrange for official transcripts and catalogs to be presented to support all credit transfers. Transcripts and records presented will remain in the student's file as part of the permanent record.

A student desiring to transfer from one DeVry Institute to another should file a request no later than the end of the tenth week of the prior term with the Dean of Students at the Institute the student is currently attending. All grades and credits earned at one DeVry Institute will be carried forward to the new Institute.

A student's financial obligation to the current DeVry Institute must be fulfilled before the transfer will be permitted. Such transfers are granted at the conclusion of the term and at no other time. It will be necessary for the transferring student to sign a new enrollment agreement or contract upon commencement of classes at the new Institute. A student on academic or disciplinary probation is ineligible to apply for a transfer.

Withdrawal

A student who wishes to withdraw from an individual course and receive a grade of W must make application to do so with the appropriate Academic Dean prior to the end of the 11th week of the trimester.

Requirements for Graduation

To graduate from any program, a student must maintain a CGPA of not less than 2.00 and satisfactory completion of all required course work. Students who complete their work in any program with a cumulative average of 3.50 or higher are graduated "with honors" and are so recognized at graduation ceremonies.

In no case will an award be made if the best recorded grade for any course is F, W or I. A student cannot omit any course listed as a graduation requirement except by a transfer of credit or by a proficiency examination.

For the Electronics Engineering Technology program, the Associate of Applied Science Degree is awarded to graduates of the seven-trimester program; the Bachelor of Science Degree in Electronics Engineering Technology is awarded to graduates of the nine-trimester program.

For the Computer Information Systems program, students who graduate after six trimesters receive an Associate of Applied Science Degree; the Bachelor of Science Degree is awarded to students who graduate from the eight-trimester program.

When a student successfully completes the five-trimester Electronics Technician program, an Electronics Technician Diploma is awarded.

The new Student Orientation and Career Development courses are requirements for graduation in all full-time programs.

Evening School graduates earn a Digital Electronics Technician Diploma after six trimesters (part time).

Degrees are conferred by DeVry Institute of Technology upon recommendation by the faculty.

Candidates for a diploma or degree who have transferred from a non-DeVry school must complete their last two trimesters at DeVry. Each candidate for graduation must have satisfied all financial obligations to the Institute at least 30 days prior to commencement.

Transfers to Other Educational Institutions

Students or graduates of DeVry Institute of Technology should note that the accepting institution has full discretion of which credits are transferable.

Financial Aid Status

Enrollment

For financial aid eligibility, a student must enroll for at least six credit hours per trimester. The amount of funding may depend on the number of credit hours for which the student is enrolled. In determining credit hour totals for financial aid eligibility purposes, credit hours for courses in which a student has enrolled more than twice or has audited are excluded.

Maximum Time Frame for Completion of the Educational Objective

A student who exceeds the maximum time frame (according to the table below) for the current curriculum of enrollment is no longer maintaining satisfactory academic progress.

Curriculum

Maximum Time Frame (Trimesters) for Completion of the Educational Objective

Electronics Technician—Diploma	7
Electronics Engineering Technology—Associate Degree	9
Electronics Engineering Technology—Bachelor's Degree	11
Computer Information Systems—Associate Degree	8
Computer Information Systems—Bachelor's Degree	10
Digital Electronics Technician—Diploma (Evening)	8

Academic Reinstatement/Readmission

Following academic expulsion:

If, due to academic dismissal, a student is out of school for one or more terms AND a readmission application is approved, financial aid eligibility will be retained.

Following academic suspension:

If an appeal of academic suspension is accepted for immediate reinstatement, financial aid eligibility will be retained.

Satisfactory Academic Progress, Academic Probation and Expulsion

If satisfactory academic progress is maintained and a student is either in good standing or on academic probation, that student is eligible for financial aid. Students readmitted based on the results of an academic suspension appeal or academic dismissal/readmission application (see Academic Appeal Process and Academic Reinstatement) will be eligible for financial aid provided other financial aid eligibility requirements are met.

Academic Withdrawals

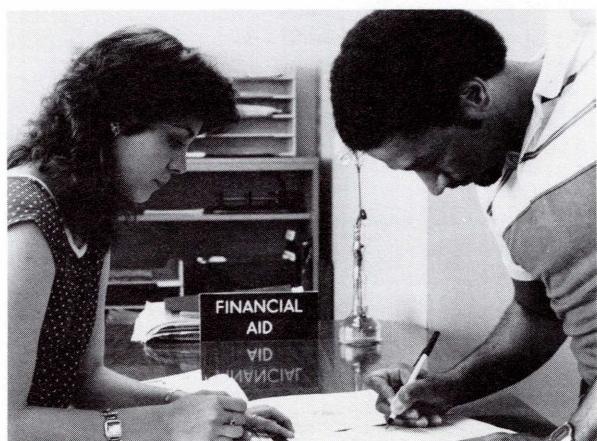
A student who withdraws from school for two successive, but not necessarily consecutive, terms prior to the end of the 12th week will lose eligibility for financial aid for the next term of enrollment. A student may regain financial aid eligibility through completion of two consecutive trimesters with both TGPAs at 2.00 or above and completion of a total of 26 credit hours, provided the resulting CGPA also is above 2.00.

Multiple Course Repeats/Audit Courses

Financial aid cannot be applied to credit hours of courses enrolled in more than twice or audited.

Financial Aid Status Appeals

Students who do not demonstrate satisfactory academic progress (see the Academic Year Progress Status section) may, under federal regulation, appeal the loss of financial aid eligibility only if mitigating circumstances exist. Financial aid eligibility appeals of the satisfactory academic progress requirements must be made in writing to the Dean of Student Finance with supporting verifiable documentation. The Dean of Student Finance will respond in writing to all appeals within 15 business days.



Financial Information

Tuition and Fees

A \$25 application fee and a \$50 tuition deposit (registration fee for Illinois students) are required of all applicants. These fees are nonrefundable unless the application is rejected by DeVry Inc. or the applicant cancels within the cancellation period as defined in the enrollment agreement/contract. The application fee must be paid at the time of application. The tuition deposit/registration fee should be paid at the time of application in order to start processing of financial aid paperwork, but must be paid within 30 days after the applicant is notified of acceptance. When the applicant is applying for a class starting in less than 90 days, both the application fee and the tuition deposit/registration fee must be made at the time of application. The balance of the student's first trimester tuition will be payable prior to the time the student starts classes.

Students enrolled in the Computer Information Systems program will be assessed a \$95 nonrefundable laboratory fee at the start of each trimester (included in the tuition calculated below).

The following tuition rates apply:

Tuition: Standard Schedules (includes tuition deposit/registration fee)

Full-time Day Programs (12 credit hours through standard schedule)

	Total Trimesters	1st Term	2nd Term	Term of Attendance 3rd and Subsequent Terms
Electronics Engineering Technology				
Bachelor's Degree	9	\$1985	\$1935	\$1825
Associate Degree	7	1985	1935	1825
Electronics Technician Diploma	5	1985	1935	1825
Computer Information Systems				
Bachelor's Degree	8	2080*	2030*	1920*
Associate Degree	6	2080*	2030*	1920*
*Includes \$95 nonrefundable lab fee				

Part-time Evening Program (six credit hours through standard schedule)

Digital Electronics Technician Diploma	6	\$1130	\$1130	\$1130
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For electronics students with standard schedules, based on the rates below, the total tuition costs including the application fee and the tuition deposit/registration fee are: Electronics Technician program (five trimesters)–\$9,420; Associate Degree in Electronics Engineering Technology program (seven trimesters)–\$13,070; and Bachelor's Degree in Electronics Engineering Technology program (nine trimesters)–\$16,720.

For computer systems students with standard schedules, tuition costs based on the rates below including the application fee, tuition deposit/registration fee and lab fees are: Associate of Applied Science Degree in Computer Information Systems program (six trimesters)–\$11,815; and Bachelor's Degree in Computer Information Systems program (eight trimesters)–\$15,655.

Tuition cost for the evening Digital Electronics Technician Diploma program (six trimesters) including the application fee and tuition deposit/registration fee is \$6,805.

Tuition for day school students with six through eleven credit hours will be half the tuition of their respective schedules. For students carrying under six credit hours, tuition will be charged at \$110 per credit hour. For those students carrying more hours than the standard schedule, tuition for the additional hours will be \$110 per credit hour.

For tuition and refund purposes, the term of attendance is defined as the actual number of trimesters, or parts thereof, that a student has attended any DeVry Institute. Thus the initial term of a student's attendance at a DeVry Institute, regardless of which program, term number or course level it is, will be considered the first term. Students returning to DeVry after interrupting their studies, having missed three or more registrations, will be considered new students in their first term of attendance.

Tuition and required fees are to be paid in advance of each trimester. If necessary, students may qualify for installment payments through the EDUCARD® Plan, an interest-bearing, revolving charge plan.

DeVry Inc. reserves the right to change tuition rates at any time. If a tuition increase is to be effected, however, it will be announced at least 90 days prior to the beginning of the term in which it will go into effect.

Students continuing from one term to the next must have their financial arrangements settled during the pre-registration period prior to the start of the new term. Any continuing student who has not completed the pre-registration procedure will be obligated to pay a \$25 late registration fee. Students repeating a course or courses will be charged additional tuition at the prevailing DeVry tuition rates.

After one term of attendance (full or part time), **evening students** are not permitted to carry an account balance over to the next trimester.

Termination and Refund Policy

The student may cancel the enrollment agreement any time prior to midnight of the fifth business day after the date of the transaction/acceptance as state law applies (cancellation period), and all monies will be refunded.

Students who do not report for class may request a refund of any monies paid to the school over and above the nonrefundable application fee and tuition deposit/registration fee, or as required by state and federal regulations.

If a student cannot start class on the original class starting date, the Director of Admissions or the New Student Coordinator must be notified. If a student starts classes within one year (three trimesters after the original starting trimester), a second application fee and tuition deposit/registration fee are not required; however, the student must sign a new enrollment agreement/contract.

To withdraw from school after attending classes, a student must complete a formal withdrawal form which can

be obtained from the Academic Dean. If no written notice is received from the student within seven calendar days of the last date of attendance, the student will be charged \$25. Withdrawal is considered official when all forms have been completed and submitted to the Institute, and all financial obligations have been fulfilled.

A student remaining in school but withdrawing from an individual course or courses, whose schedule status does not change (standard schedule, half time, part time) will not receive a tuition adjustment. A student remaining in school but withdrawing from an individual course or courses, whose schedule status does change from standard or half time to half time or less will not receive a tuition adjustment for status changes after Friday of the second week of the term.

Regarding tuition refunds, termination will be as of the last date of attendance. The initial term of a student's attendance at a DeVry Institute, regardless of which term number it is, will be considered the "first term" for both tuition and refund determination. Similarly, the student's second term of attendance at DeVry will be considered the "second term," and so forth, regardless of whether the terms were completed. This also applies to transfer students and students who are returning to school after interrupting their studies and missing no more than two registrations. Resuming students who have missed three or more registrations are considered first-term students.

For the tuition refund schedule applicable to your Institute or state, please see the section entitled *Refund Schedules* in this catalog.

Failure to Fulfill Financial Obligations- Withholding of Services

For those students who fail to pay their tuition, fees or other charges, DeVry reserves the right to: dismiss the student from school; withhold the student's transcript; withhold the student's diploma or degree, if graduating; and/or withhold graduate placement assistance from the student, if graduating or already graduated. However, students will remain responsible for all tuition, fees and charges incurred in accordance with DeVry's refund schedule.

Textbooks and Supplies: The cost of textbooks and supplies will range from \$75 to \$200 on a per-trimester basis. Prices are subject to change depending on the publishing prices. Textbooks may be purchased through the school or from an outside bookstore, but must be consistent with the required texts.

Students in the electronics programs should purchase calculators. Appropriate calculators range from \$20 to

\$50. Students should check with the instructor prior to purchase to be certain it meets the requirements of the curriculum.

Transcript Fee: One transcript will be made available to each student at no charge. A \$2 charge is made for each additional transcript that is requested. Requests for grade transcripts must be in writing. Official transcripts for work completed will not be issued until all obligations to DeVry have been met.

Student Activity Fee: A one-time, nonrefundable student activity fee is charged by the DeVry Student Association at the time new, full-time students register. The fee, not to exceed \$25, is due at registration and entitles students to reduced or free admission to all Student Association activities. These may include movies, dances, picnics and special events. In addition, this fee helps support clubs, intramural sports and the school newspaper.

Laboratory Fee: There is no additional laboratory fee for electronics programs except in the case of lab equipment breakage. A charge will be levied against students for excessive waste, loss or damage. This charge must be paid before credit will be given for the course. The costs of normal usage and replacement of equipment used in

the lab experiments are included in the tuition. The lab fee for the Computer Information Systems programs is included in the tuition.

Insurance: All full-time students will be required to enroll in a special group accident and emergency illness insurance plan unless they are covered under a similar insurance program (individually or as dependents under their parents' insurance). Coverage will remain in force during the period for which the premium has been paid (24 hours per day) whether the student graduates, withdraws or is on vacation.

This insurance program has been specially designed for DeVry students by the Guarantee Trust Life Insurance Company of Glenview, Illinois, at an annual premium cost of either \$80 or \$106, depending on the benefit option selected (added to student fees) for a 12-month, nonrefundable period. This plan also provides the option of coverage for spouse and children. Forms and information are available through the Student Services Office. Insurance rates are subject to change.

Graduation Fee: Candidates for graduation from all programs may be charged a fee, not to exceed \$25, to cover costs of the ceremony, diploma and speakers.



Parking Fee: Students are required to purchase a parking permit (not to exceed \$15 per trimester) to be able to park in school parking lots. Students must adhere to all parking rules or be subject to disciplinary action and towing of their vehicles. (See Student Handbook for details.) Purchase of a parking sticker does not guarantee a parking space.

Late Registration Fee: A late registration fee of \$50 will be charged to continuing students who fail to register before the end of the designated registration period.

Unpaid Check Fee: A fee of \$10 will be charged for each check returned by the bank due to nonsufficient funds.

Veterans Benefits

DeVry Institute of Technology's day and evening Electronics and day Computer Information Systems programs are approved by the State Approval Agency for veterans benefits, except DeVry, Los Angeles. (DeVry, Los Angeles will apply when eligible.) Monthly educational allowances are paid by the Veterans Administration directly to the veteran student or other eligible person to help defray the cost of tuition and living expenses. Actual amounts are available upon request. Disabled veterans may be eligible for complete rehabilitation training. Eligibility is determined by the Veterans Administration.

Students who will qualify for veterans educational benefits should submit the appropriate application benefits

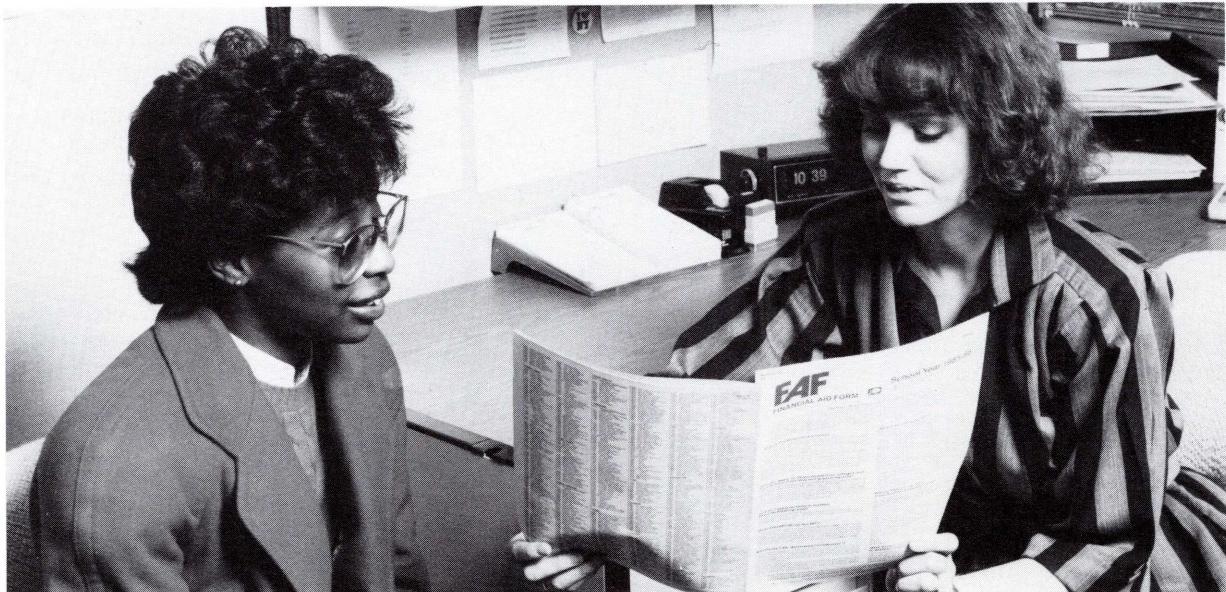
form, along with discharge papers, as far in advance of the scheduled class starting date as possible. Details may be obtained from the Institute.

Refunds for veterans and eligible persons enrolled in programs approved under Section 1775 of the G.I. Law are the same as indicated under the Tuition Refunds section. All programs at the Dallas Institute are approved under Section 1775.

Regarding refunds for veterans and eligible persons enrolled in programs approved under Section 1776 of the G.I. Law, a refund of the unused portion of tuition, fees and other charges will be made to veterans or eligible persons who fail to enter or fail to complete the courses as required by Veterans Administration regulations. The refund will be within 10 percent of an exact pro rata refund. No more than \$10 of the established registration fee will be retained if a veteran or eligible person fails to enter the course. Students may secure additional details by contacting the Institute.

The Veterans Administration must be notified when the veteran is not meeting the Institute's academic progress standards (as described in the Admission and Academic Information section). Veterans do not qualify for benefits for courses which are voluntarily repeated or audited.

If a student has not previously applied for or received benefits under the current law, DeVry will help the veteran make arrangements with the Veterans Administration.



Financial Assistance

DeVry Inc. is committed to assisting its students in developing financial plans for their educations through a combination of government grants and loans, family contributions, outside sources of aid and the EDUCARD® Plan.

Students will also be assisted in applying for available government grants, veterans benefits and other benefits for which they may be eligible. If necessary, help will be provided in applying for student loans from eligible lending institutions.

A variety of methods for meeting school expenses is available to DeVry students. To help select the method or methods best suited to the student's needs, the Financial Aid Form is used to calculate the various educational expenses and determine what requirements the student may have, if any, in meeting those expenses.

On the basis of the Financial Aid Form completed by the student and family, an evaluation of available resources is made by the Institute. The Financial Aid Form Need Analysis Report will indicate the amount of financial assistance that may be needed and the family's expected contribution. This analysis is prepared by the College Scholarship Service for a small fee. The results help DeVry compare the expenses students can expect during the course of their educational program with funds that may be available for payment of the expenses. **These funds will be applied to tuition and fees first.**

The Financial Aid Form must be completed and returned within two weeks of application or receipt of paperwork. Prompt return of this form assures the student of maximum consideration for available financial aid. DeVry will then send award letters advising students of the total financial aid for which they may be eligible. Failure to submit the required financial aid paperwork and/or EDUCARD® Plan payment within the requisite time period may result in a request for full payment of tuition and fees.

Financial aid cannot be applied to credit hours of courses completed more than twice or courses that are audited.

Upon receipt of the student's completed Financial Aid Form, an assistance package is worked out for students who require financial help. To the extent possible, students are expected to help themselves. Self-help includes cash payments individual students are able to make out of savings, part-time job earnings and assistance from parents, guardians or other family members.

If, due to expulsion, a student is out of school for one or more trimesters **AND** a readmission application is approved for the original curriculum or for transfer to another curriculum, he/she will retain financial aid eligibility.

There is no assurance that DeVry Inc. will grant any student requesting financial assistance any loan or credit extension, although DeVry Inc. will take any and all reasonable steps to assure the availability to all students making such a request.

To help students pay for their educations after high school, the United States Department of Education offers six major financial aid programs. DeVry Institutes are eligible to participate in all six.

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1. Pell Grant Program
 2. Supplemental Educational Opportunity Grant (SEOG)
 3. National Direct Student Loan Program (NDSL)
 4. Guaranteed Student Loan Program (GSL)
 5. PLUS/ALAS Loan Program
 6. College Work-Study
-

In general, students are eligible for aid if they:

- are enrolled at least half time as regular students in an eligible program;
 - are U.S. citizens or eligible noncitizens (i.e. permanent residents);
 - show that they meet the need requirements specified;
 - make satisfactory academic progress toward completing their course of study;
 - are not in default on an NDSL, GSL or PLUS loan;
 - do not owe a refund on a Pell Grant or SEOG;
 - have filed a signed Statement of Educational Purpose/Registration Compliance with the Institute, as required by law of both males and females.
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Pell Grants

Pell Grants provide money to help undergraduates (who have not earned a Bachelor's Degree) pay for their educations after high school. The Pell Grant Program is the largest of the federal Student Aid Programs. Unlike loans, grants do not have to be paid back. For many students, these grants provide a "foundation" of financial aid, to which aid from other federal and nonfederal sources may be added. The government encourages all students to apply for Pell Grants, and DeVry requires it if students are applying for any Title IV financial assistance or the EDUCARD® Plan privilege.

SEOG

The Supplemental Educational Opportunity Grant (SEOG) is a federal government program designed to provide supplemental funds to needy students. Funds are distributed to schools by the government which then determines the student's eligibility. Funds are limited, so students are encouraged to apply early. More information on these grant programs is available from the Financial Aid Office. Students must complete the Financial Aid Form to be considered for SEOG funds.

NDSL

DeVry Institute is also eligible to participate in the National Direct Student Loan (NDSL) Program. Students who demonstrate financial need may be awarded an NDSL. Loan amounts cannot exceed \$3,000 for the first two years of study or \$6,000 toward the completion of the Bachelor's program. Repayment plus 5% interest begins six months after the borrower ceases to be at least a half-time student. The minimum monthly payment is \$30, and the total debt must be repaid within a maximum of 10 years. More information is available from the Financial Aid Office. Like the SEOG program, students must complete the Financial Aid Form to be considered for NDSL funds.

GSL

Under the Guaranteed Student Loan (GSL) program, students can apply for a loan to help pay the cost of their tuition, books, supplies, fees and living expenses. Such loans are often available through banks, credit unions, and savings and loan associations. Local institutions of this type should be the first sources contacted when a student loan is necessary. Students who use the loan program qualify for a "no-interest" provision while they are in school and for the first six months after graduation.

The amount of money a student may borrow will depend on the results of the need test, but may not exceed \$2,500 per year. Students are required to pay a 5% origination fee to the lender as authorized by federal law. Students begin repaying the loan six months after they graduate or discontinue their studies at an interest rate of 8% per year, simple interest. The minimum monthly payment is \$50. Repayment of these loans is usually completed in 10 years or less. When a student leaves school, the lender contacts the student to establish a repayment schedule.

PLUS/ALAS

The federal government authorizes State Loan Programs and local banks to allow the parents of dependent students to borrow up to \$3,000 for educational expenses under a program called PLUS (also known as Auxiliary Loans to Assist Students). An independent undergraduate may also borrow under PLUS as long as the amount borrowed under GSL and PLUS programs does not exceed \$2,500. Repayment begins 60 days after borrowing. The current interest rate is 12%. More information is available from the Financial Aid Office.

College Work-Study

College Work-Study is a federal program that allows students who demonstrate financial need to earn a portion of their educational expenses by working for a nonprofit agency. Students will be paid at least the current, hourly minimum wage.

The DeVry Financial Aid Office will assist students in locating a job with an off-campus public or private nonprofit agency doing work in the public interest. Earnings may be used to help pay tuition and fees or living expenses.

EDUCARD®

DeVry's open-end, interest-bearing, revolving charge account also serves as the billing system of DeVry Inc. The EDUCARD® Plan gives the student an affordable payment program that is worked out in accordance with individual financial circumstances reflected in the financial information submitted to DeVry. The EDUCARD® Plan's function as an open-end, revolving charge account is conditioned upon the student exhausting all other avenues for student financial assistance, including government grants and loans, if eligible.

The EDUCARD® Plan's primary objective is to finance tuition costs for students until the financial aid to which they may be entitled is received. It further enables students to pay for the balance of their charges in monthly installments during the trimester and provides extended credit under approved circumstances when financial aid is insufficient or the student is ineligible to receive financial aid.

The first monthly EDUCARD® Plan payment is due at registration. As in the case of any charge account, delinquencies in payment result in a curtailment of credit privileges. If such delinquencies are not resolved, the result

may be the student's financial suspension from school. In the event that credit is denied any student by DeVry Inc. under its EDUCARD® Plan, the student may withdraw from classes and receive a credit and/or refund of the tuition costs in accordance with the refund policies contained in the Termination and Refund section.

Part-time Employment Assistance

The DeVry Institute Student Employment Assistance Office will help students find part-time jobs while they are attending school. The majority of students work part-time and are able to help meet living expenses through their earnings. New students are eligible for this employment assistance service after the first day of classes.

Since employment depends on local business conditions, we cannot guarantee jobs. However, the record of part-time placement at DeVry is impressive.

Beginning students should not expect part-time jobs to be in the electronics or computer fields, but in a wide range of areas. Starting salaries range from \$3.35 to \$4.50 per hour. Students who remain on the job for a while can usually expect regular increases.

The only restriction on part-time employment is that it not be detrimental to the student's health and scholastic progress. Ordinarily, work schedules beyond 25 hours per week are not advisable.

Scholarships

The DeVry Inc. Scholarship Competition annually offers 75 full-tuition scholarships valued between \$14,700 and \$15,750. Each scholarship covers the application fee and tuition for the Bachelor's Degree program in either Electronics Engineering Technology or Computer Information Systems. Scholarship recipients are responsible for their own books, lab fees, supplies and living expenses.

Applicants must be in their senior year of high school with credits for at least one year of science and one year of algebra. Semifinalists will be determined on the basis of performance on the Scholastic Aptitude Test (SAT) of the College Entrance Examination Board, the American Colleges Test (ACT) or the Washington Pre-College Test (WPCT). The SAT, ACT and WPCT tests must be taken on specified dates which are announced each year to high school counselors. Winners of the scholarships will be determined on the basis of their high school academic record/activities, an essay and their SAT/ACT/WPCT scores.

Scholarship students must maintain a 2.00 CGPA in order to maintain the scholarship. In the event the CGPA drops below 2.00, scholarship monies will not be received, and the student will be responsible for future tuition. The scholarship can be reinstated when the CGPA is raised to 2.00. Students who drop below 2.00 a second time will be dropped from the program and will not be reinstated.

For more information or application forms, students should contact their high school counselors or the Director of Admissions at DeVry Institute of Technology.

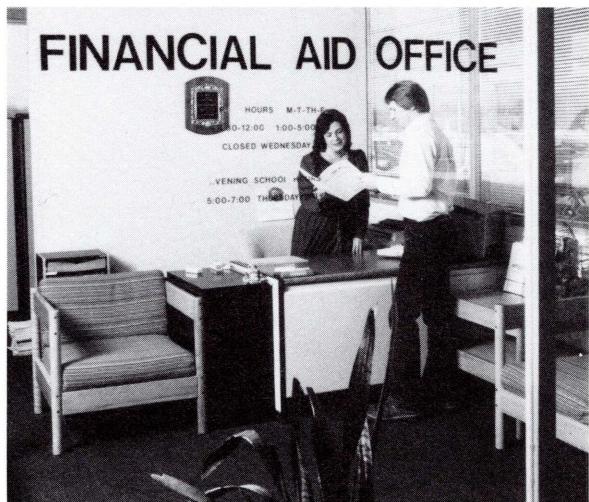
Students may take advantage of other applicable scholarships which may apply to study at DeVry. Information on alternate scholarship programs (which may be based on academic performance, financial need or family affiliations) is available through high school guidance counselors. Students should investigate these scholarships, as many are not widely publicized.

Fellowships

DeVry offers a limited number of full-tuition fellowships for secondary school teachers. Fellowships cover tuition and laboratory fees for one or two trimesters of study in any of the programs offered at DeVry.

Applicants must be regularly employed teachers at public or private secondary schools in the fields of electronics, computer science, business, mathematics or physical sciences.

Interested teachers should write for additional information, including application forms, to the Director of Admissions at the Institute.



CAREER COUNSELING
AND
PLACEMENT OFFICE



Student Services

Graduate Placement/Career Counseling

The Institute maintains an active Career Counseling and Placement Office (CCPO) to assist graduates of all programs in attaining positions in the fields of electronics and computer information systems that suit their backgrounds and interests.

While enrolled in school, students are contacted by the CCPO staff who assists and advises students seeking employment. The CCPO staff members advise students on job-interviewing techniques and on preparing resumes.

Near graduation, the CCPO staff notifies students of job openings so that interviews with various companies can be scheduled. In many cases, company representatives conduct interviews at the Institute.

While the Institute can neither guarantee jobs nor assure students of income opportunities, the career counselors can greatly aid in the job-hunting process. If students place restrictions on employment, such as location, their employment options may be similarly restricted. The CCPO staff will continue to work with graduates as long as they actively pursue assistance.

The work of the Office is in conjunction with CCPOs at other DeVry Institutes. Jointly, the CCPOs maintain contact with hundreds of companies in the electronics and computer fields throughout the United States and in Canada, developing employment opportunities for DeVry graduates.

Each CCPO is equipped with an on-line computerized communications system linking all DeVry Institutes together. The objective of the DeVry Employment Activity Network (DEAN) is to match students with employers. Detailed employment information stored in DEAN is updated regularly and available for students' use before and after graduation.

In order to find satisfying employment after leaving DeVry, students are encouraged to use the facilities at their Institute's CCPO. The first step is to allow the Placement Office to release information including resumes, academic transcripts and references to prospective employers. A copy of the student's resume must be brought to the CCPO no later than the eighth week of the student's last trimester at DeVry. With the help of a mailing service, the first 50 copies of the resume will be sent to prospective employers before graduation. The student must participate in at least one individual counseling session with a CCPO career counselor and attend all career presentations given by the CCPO.

After graduation, students who have not yet obtained employment are expected to actively pursue employment while receiving continued assistance from the CCPO. Actively pursuing employment includes maintaining a close relationship with the Office by making a visit or phone or mail contact at least once every two weeks. Also, during the first 17 weeks after graduation, graduates are expected to make a personal visit or phone call to an average of three companies each working day. After the graduate has accepted a job offer, he/she is expected to notify the CCPO to ensure both registration in the DeVry Alumni Association and future employment assistance.

DeVry also operates field placement offices to further assist graduates in finding employment. These offices are located in: San Francisco, CA; Los Angeles, CA; Ft. Lauderdale, FL; Dallas, TX; and Baltimore, MD. Each office is staffed by a Regional Manager of Graduate Employment (RMGE) who seeks out new companies to recruit on each of the DeVry campuses. The RMGE also will attempt to set up interviews for graduates at the company site if industry representatives cannot recruit on campus. It is for this reason that graduates should be in the general area of the field office to be available for interviewing. And, if they are offered jobs, they should be willing to relocate at their own expense.

The Institute expects and assumes that all students and graduates who use Graduate Placement's services will abide by DeVry's Graduate Placement Code of Conduct. A copy of the Code is available in the CCPO or RMGE office.



DeVry Alumni Association

Once a student graduates from DeVry, he/she is eligible for membership in the DeVry Alumni Association. The Association promotes professional networking, information exchange and career growth through reunions, regional chapter meetings and seminars. Members may be offered various benefits (for example, discounts on computers, electronics products and life insurance). Through the association, both members and non-members can participate in alumni career assistance programs. No dues or donations are required for membership.

Housing

DeVry maintains a Student Housing Office whereby several living options are made available to students.

The Housing Office maintains an updated list of apartments in the general vicinity of the school. Most apartment complexes/owners require advance payment equal to the first month's rent as a security deposit. Many times a previous rental or credit history will also be required. Individual leasing terms are established between the apartment complexes/owners and the tenants.

DeVry also maintains listings of private rooms for student use. These rooms are in private residences and are available on a limited basis. The type of accommodation will vary depending on location. Individual leasing terms are established between the owner and the tenant.

DeVry Student Plan Housing is an option offered by some DeVry Institutes. Apartment units are secured and housing is arranged through the Institute. With this option, students submit their security deposit/reservation form to DeVry in order to reserve a furnished, shared apartment. All subsequent rental installments are made to the Institute. Student Plan Housing is offered in order to provide convenient affordable housing for students during their initial terms.

For additional information on housing, refer to the Housing Information packet or contact the Student Housing Office at the Institute. All students who need help in locating housing or have problems related to their living arrangements are encouraged to visit the Housing Office.

DeVry is committed to a policy of nondiscrimination in housing, and all housing to which students are referred must comply with this policy.

Counseling Services

Advising students on personal matters is an important function of the Student Services Office. Students are encouraged to consult a Student Advisor concerning any problems affecting course work, career plans and even for suggestions for leisure-time activities.

Prior to registration for classes, new students can seek advice through the Director of Admissions Office. The Director and staff offer guidance in the problems which commonly confront new students, such as financial troubles. They will also direct students to an appropriate advisor, if needed. The Graduate Placement Office offers career counseling, and the Academic Department provides advice on academics.

Each Institute is also staffed with a New Student Coordinator (NSC). At DeVry, the NSC's job is to assist applicants as needed, prior to the first day of classes. Questions that students and parents may have can be addressed to the NSC. Phone calls to the NSC can be made collect where a toll-free number is not available.

All faculty members will take the time to discuss individual problems students are having in their courses. Students are encouraged to make appointments with the faculty. Conferences will normally be arranged for after school hours, during breaks between classes or during scheduled office hours. Whenever difficulties in a course are encountered, students should first confer with a faculty member. If necessary, the student may consult the Academic Dean.



Class Size

Conventional lecture classes range in size from small, 15 or less, to as many as 80 students. An innovative instructional method used at DeVry is the Lecture Support Team (LST) concept. Lecture Support Teams are comprised of faculty members and faculty assistants. Designed to improve learning, LST incorporates small group-based problem-solving activities as well as other forms of teaching in classes ranging from 100-130 students. Extra help is also available for those students who require it.

Laboratories

Labs are available during scheduled hours during the school day and may be available after school only at some Institutes. Students who are doing additional work on an assigned project or working on a project of their own are encouraged to use the facilities, provided they do not disturb a class in session and provided that permission is obtained from the appropriate instructor.

Electronics laboratory facilities include: 1) student work spaces, each equipped with an oscilloscope, signal generator, multimeter and power supply. (These work

stations are utilized for basic experiments in electronic circuits; 2) advanced labs, equipped to complement course work in digital circuits, digital computers, microprocessors, communication systems, industrial electronics and control systems; and 3) a physics lab.

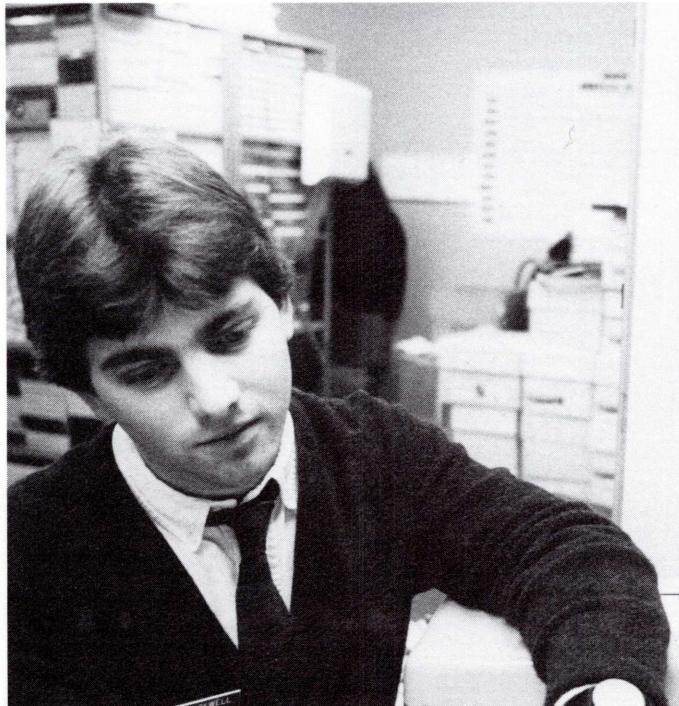
The Computer Information Systems laboratory facilities include: IBM terminals and data communications equipment connected to a central-site IBM computer; local minicomputer spooling and printing capability and, for certain courses, access to microcomputers.

Learning Resource Center

The Learning Resource Center (LRC) contains technical journals, periodicals, books and reference materials. Students are encouraged to use the LRC facilities. Students are held responsible for all materials checked out and will have to bear the cost of replacement if materials are lost or damaged.

Bookstore

Textbooks and required supplies, such as parts and kits for lab projects, are available in the school bookstore. The bookstores at some Institutes carry a number of supplementary books and supplies.



Regulations

Rules and Conditions of Enrollment

DeVry expects and assumes mature behavior of its students and strives to create and maintain an environment of social, moral and intellectual excellence. DeVry reserves the right to dismiss any student whose work or conduct is unsatisfactory. An explanation of the Code of Conduct and the disciplinary process is provided in the Student Handbook or can be obtained from the Student Services Office.

Privacy Act

DeVry Institute of Technology complies with the Family Educational Rights and Privacy Act of 1974. This Act was designed to protect the privacy of education records, to establish the rights of students to inspect and review their education records, and to provide guidelines for the correction of inaccurate or misleading data through informal and formal hearings.

The Institutional Policy explains the procedures used by the Institute for compliance with the provisions of the Act. Copies of the policy can be found in the Student Services Office or Student Handbook.

Title VI

DeVry Institute of Technology is in compliance with Title VI of the Civil Rights Act of 1964 and does not discriminate on the basis of race, creed, color or national origin.

Title IX

Title IX of the Educational Amendments of 1972 prohibits discrimination, on the basis of sex, in any educational program or activity receiving federal financial assistance. DeVry Institute of Technology complies with this act. DeVry Institute of Technology does not discriminate, on the basis of sex, in its educational programs, activities, admission or employment. Inquiries or complaints concerning the application of Title IX may be referred to the Dean of Students at the Institute or to the Director of the Office of Civil Rights for the Department of Education in Washington D.C.

Section 504

DeVry Institute of Technology is in compliance with Section 504 of the Rehabilitation Act of 1973 and does not discriminate on the basis of handicap.

Changes/Cancellations of Programs

The sequence of DeVry's courses may vary, and the Institute reserves the right to revise or delete courses for purposes of upgrading curricula. If it becomes necessary for any reason to interrupt its regular class schedules or starting dates, then the Institute may, upon reasonable advance notice, suspend or cancel instruction.

The Institute reserves the right to cancel a starting class (a class beginning the first term of an academic program) if the number of students enrolling is deemed insufficient. In the event of a program or class cancellation, students will be offered the opportunity to transfer within the DeVry Inc. system with full credit for all work completed. Not all programs are offered at all Institutes.

Should a situation arise in which more enrollees arrive at the Institute to start classes than can be accommodated, those who cannot start will be given the following choices: (1) a transfer to another DeVry Inc. Institute which offers the program, in which case DeVry Inc. will pay the applicant's transportation costs, for only that first trimester, to the other Institute; (2) a refund, within 30 days, of the tuition deposit/registration fee and application fee paid. Cost of round-trip transportation in both instances (most economical, as selected by DeVry Inc.) from the applicant's home to the Institute will also be paid for only that first trimester.

In any case of cancellation or interruption, a full refund will be made of any fees or tuition prepaid by the student for that trimester, unless the student elects to transfer to another DeVry Institute, in which case the funds will also be transferred.

If the length of programs must be changed, then tuition for any additional course work will be charged at the rate prevailing at the time course work is taken.

Attendance

The Institute reserves the right to make class session assignments and to reschedule assignments, if and when necessary.

Each student will be expected to attend every meeting of every class in which the student is registered. Attendance will be recorded at least once each class session for all students and hourly for certain agency-sponsored students. When absence is recorded by session, the faculty member will record the total session hours missed. It is the students' responsibility to inform the faculty member if they arrive after attendance is recorded. In case of unavoidable absence, it is the students' responsibility to explain the absence to the professors immediately upon returning to class.

Students are responsible for all work missed during their absence, and they also must contact their professors concerning individual make-up work immediately upon return. Students who have been or anticipate being absent from all classes for three or more consecutive days must report to the Academic Office.

A student will be suspended immediately from all classes if absence occurs for five consecutive class days. Students in the first or second term of a curriculum or students with CGPAs of less than 2.50 will be placed on attendance warning and may be contacted for counseling by the Academic Dean (or designee) if their hours of absence in a course exceed 20% of the published elapsed hours from the first day of the trimester. Counseling may result in setting criteria for suspension, either from an individual course or from all courses.

A student on attendance warning may appeal in writing to the Academic Dean within three class days after notification. The appeal package must contain documentation of a valid reason for the absence, (for example, doctor's report of serious illness, death in the family and/or other pertinent information). During the period of appeal after an attendance warning notification, the student may attend class and take examinations. If the resulting decision of the appeal to the Academic Dean is disapproval, the student will be suspended. The student may make a final written appeal to the Dean of Academic Affairs no later than the end of the class day following the disapproval. Students are expected to be present at the beginning of each class meeting. Cases of excessive tardiness may be referred to the Academic Dean for disciplinary action.

Disciplinary Action

Students who commit a breach of Institute rules or standards of good conduct will be referred to the Office of the Dean of Students. An investigation of the facts surrounding the situation will be made. Students will be advised of the facts disclosed by the investigation and will be given an opportunity to question evidence and present witnesses and affidavits on their own behalf.

The Dean of Students, or designated representative, may take the following actions: dismiss the case; give the student an official warning; or process a formal probation, suspension or expulsion action. Disciplinary action varies with the severity of the violation. Disciplinary action may be appealed to the Dean of Student Services and Administration.

All records of disciplinary actions and proceedings are highly confidential and are maintained only in the Student Services Office. Permanent records will only be maintained upon a student's expulsion from the Institute. Details are available from the Student Services Office or the Student Handbook.



Electronics Engineering Technology Course Descriptions

The student is eligible for a Bachelor of Science Degree after successfully completing the nine-trimester (a trimester is 15 weeks in length) Electronics Engineering Technology curriculum.

Upon satisfactory completion of the first seven trimesters of the Electronics Engineering Technology program, the student is eligible to receive the Associate of Applied Science Degree.

The first seven trimesters provide instruction in such subjects as communication systems, digital computers and control systems. Mathematics is stressed with application to practical design problems and to systems analysis. The student works with a variety of equipment similar to that found in industry.

The remaining two trimesters provide instruction in advanced and specialized areas of electronics technology and in business, management and the liberal arts. The Bachelor of Science Degree is earned upon successful completion of the full nine trimesters.

DeVry Inc. reserves the right to alter the total number of contact hours listed for reasons including, but not limited to, the following: natural occurrences beyond DeVry's control, holidays, special institutional activity days and registration days.

NOTE: In the three numbers after each course title, the first refers to the hours per week in the classroom, the second to the hours per week in the laboratory and the third to the number of credit hours.

First Trimester

Circuit Analysis I (CA-103) [6-0-6]

This course covers the analysis of circuits from a direct current standpoint and provides an introduction to alternating current. Topics include current, voltage, resistance, power and energy, series and parallel circuits, combination circuits and network theorems. (Prerequisite: concurrent enrollment in A-102)

College Algebra (A-102) [4-0-4]

This first course in the mathematics sequence includes the fundamental laws of algebra, functions and their graphs, linear equations, systems of linear equations, quadratic equations, factoring and determinants.

Structured BASIC Programming (BP-101) [4-0-4]

BASIC language programming is introduced in the first term to provide the student with a solid foundation in the use of a computer as a problem-solving tool. The course emphasizes structured programming techniques. Topics include logic and problem solving, flowcharts, BASIC language syntax, modular structures and debugging. BASIC language programming assignments will be made in courses through the technology curriculum. (Prerequisites: concurrent enrollment in A-102 and CA-103)

Student Orientation (SO-102) [1-0-0]

This is an introduction to DeVry programs and to the personal and academic challenges of college life. The course explores the curriculum and the requirements for graduation, career options, study skills and personal-management strategies.

Circuit Analysis I Laboratory (CA-103L) [0-2-1]

This laboratory course provides practical experience related to topics in the Circuit Analysis I course. Emphasis is placed on developing familiarity with electronic components, breadboarding techniques and the proper use of test equipment such as electronic power supplies, multimeters, signal generators and oscilloscopes. (Prerequisites: concurrent enrollment in CA-103 and A-102)



Structured BASIC Programming Laboratory (BP-101L) [0-2-1]

This laboratory course provides hands-on computer programming experience to support the Structured BASIC Programming course. Emphasis in the laboratory is placed on problem-solving techniques, flow charting and documentation. Students develop and run programs that provide a wide range of experience in structured programming techniques. (Prerequisites: concurrent enrollment in BP-101 and A-102)

Second Trimester

Circuit Analysis II (CA-203) [4-0-4]

A continuation of Circuit Analysis I, this course deals primarily with alternating current concepts and circuits. Topics include capacitors, inductors, AC series/parallel/combination circuits, AC network theorems, transformers, passive filters and response curves. The student is introduced to the use of a computer to design passive filter circuits. (Prerequisites: CA-103, BP-101 and concurrent enrollment in T-201)

Electronic Devices and Circuits I (ED-203) [4-0-4]

This course presents the basic concepts of electronic devices such as bipolar junction transistors, semiconductor diodes, Zener diodes, field-effect transistors and four-layer devices such as the SCR and triac. Emphasis is placed on device characteristics and operating conditions. The student is introduced to the basic circuits for each of the devices, circuit analysis of low-frequency amplifiers, and computer techniques used to determine operating characteristics. (Prerequisites: CA-103, A-102 and concurrent enrollment in CA-203)

Trigonometry (T-201) [5-0-5]

The second-term mathematics course presents concepts and logic patterns prerequisite to the study of calculus. Major topics include vectors, right triangles, oblique triangles, complex numbers, trigonometric functions, exponential and logarithmic functions, and logarithmic equations. (Prerequisite: A-102)

Psychology (PSY-201) [3-0-3]

This course explores the basis of individual behavior through a study of human development, theories of personality, and adjustment to family, work and society. Related topics include learning and memory, communication and mass persuasion techniques.

Circuit Analysis II Laboratory (CA-203L) [0-2-1]

This course provides practical experience related to the topics in the Circuit Analysis II course. The student is exposed to more advanced measurement techniques and the use of test equipment in the analysis of passive circuits. (Prerequisites: CA-103L and concurrent enrollment in CA-203)

Electronic Devices and Circuits I Laboratory (ED-203L) [0-2-1]

The laboratory exercises associated with this course provide practical experience related to the Electronic Devices and Circuits I course. Emphasis is placed on breadboarding active circuits including bipolar junction transistors, field-effect transistors and control devices such as SCRs. (Prerequisites: CA-103L and concurrent enrollment in ED-203)

Third Trimester

Electronic Devices and Circuits II (ED-302) [4-0-4]

The second course in the devices and circuits sequence emphasizes the use of active devices in low- and high-frequency amplifier circuits. Both junction and field-effect transistors are modeled. Topics include bias circuits, single-stage analysis, multi-stage analysis, frequency response, Bode plots and negative feedback. The student is introduced to the computer techniques used for the analysis and design of amplifier circuits. (Prerequisites: ED-203 and CA-203)

Digital Circuits (DC-302) [4-0-4]

This course serves as an introduction to digital technology. Emphasis is placed on integrated circuits. Topics include number systems and codes, truth tables, Boolean functions, combinational logic, sequential logic, registers, counters and device characteristics. (Prerequisites: ED-203, CA-203 and concurrent enrollment in ED-302)

Calculus I (C-302) [5-0-5]

This first course in calculus covers limits, differentiation of algebraic functions, conic sections, differentiation of implicit and explicit functions, definite and indefinite integrals, and differentiation and integration of trigonometric, inverse trigonometric, exponential and logarithmic functions. The student is also introduced to numerical techniques for computer solution such as the trapezoid rule. (Prerequisite: T-201)

English (E-301) [3-0-3]

This course provides a review of grammar, usage and punctuation, and applies these basic tools to the writing of effective paragraphs and essays. Through analysis of models, writing practice and revision techniques, the goals of unity, coherence and logical development in compositions are pursued.

Electronic Devices and Circuits II

Laboratory (ED-302L) [0-2-1]

This laboratory course provides practical experience related to the topics in the Electronic Devices and Circuits II course. The student is exposed to more advanced breadboarding, analysis and measurement techniques. Effects of circuit component values on gain and frequency response are studied. Feedback amplifiers are analyzed, and the student practices troubleshooting techniques. (Prerequisites: ED-203L and concurrent enrollment in ED-302)

Digital Circuits Laboratory (DC-302L) [0-2-1]

This laboratory supports the Digital Circuits course and includes topics on TTL/MOS logic circuits, combinational logic, sequential logic, counters, registers and Boolean simplification. (Prerequisites: CA-203L, ED-203L and concurrent enrollment in DC-302)

Fourth Trimester

Electronic Devices and Circuits III (ED-402) [4-0-4]

This third course in the devices and circuits sequence is devoted to operational amplifiers and related circuit applications. Topics include the differential amplifier, inverting and non-inverting amplifiers, resistive and RD feedback, gain, slew rate, response curves, instrument amplifiers, active filters, Wien bridge oscillators, comparators and limiters, switching power supplies, voltage regulators and phase-locked loops. (Prerequisites: ED-302 and DC-302)

Digital Systems (DS-401) [4-0-4]

This second course in the digital sequence includes systems applications of digital circuits such as memory circuits, A/D and D/A converters, RAM and ROM devices and programming, and a complete sequential logic system. This course also provides an introduction to microprocessors with topics including microprocessor architecture, addressing modes, instruction cycles, instruction sets and an introduction to assembly language. (Prerequisite: DC-302)

Calculus II (C-402) [5-0-5]

A continuation of Calculus I, this course covers hyperbolic functions, methods of integration, arithmetic and geometric series, Fourier series, partial derivatives and double integrals. The course also provides an introduction to differential equations and basic statistics. Computer techniques are used to perform numerical and statistical analysis. (Prerequisite: C-302)

Technical Writing (TW-401) [3-0-3]

This course is an application of general composition principles to common report formats used in industry and to common types of business writing. Work is also devoted to audience analysis, development of effective technical style, organization methods, graphic aids and the preparation of a formal library research paper. (Prerequisite: E-301)

Electronic Devices and Circuits III Laboratory (ED-402L) [0-2-1]

This course provides practical experience with linear integrated circuits and applications in support of the Electronic Devices and Circuits III course. Topics include the analysis and design of operational amplifier circuits including inverting and non-inverting amplifiers, integrators and differentiators, oscillators and active filters. (Prerequisites: DC-302L and concurrent enrollment in ED-402)

Digital Systems Laboratory (DS-401L) [0-2-1]

Laboratory exercises in this course support the Digital Systems course. The student breadboards a variety of digital systems such as D/A and A/D converters. A complete sequential system, such as a frequency counter, is also examined. This course also provides an introduction to microprocessors. Students gain experience in instruction sets by developing several program modules. (Prerequisites: DC-302L and concurrent enrollment in DS-401)

Fifth Trimester

Transform Analysis and Applications (TA-501) [4-0-4]

This course emphasizes the analysis of electric circuits and physical systems through the Laplace transform. Topics include partial fraction expansion, transfer functions, step inputs, pole-zero diagrams, network equations in Laplace form, and applications to passive and active filters. Computer-based numerical techniques are employed to solve practical problems. The course also provides an introduction to the z-transform and its applications to digital systems. (Prerequisite: C-402)

Microprocessor Hardware and Software (MS-501) [4-0-4]

This course emphasizes the hardware and software relationships in a microprocessor. Topics include bus timing, I/O timing, serial I/O, parallel I/O, use of monitor subroutines and control signals. The student also applies structured programming techniques to microprocessor assembly language programs such as time delays, multiple precision arithmetic and base conversions. (Prerequisite: DS-401)

Physics I (P-502) [4-0-4]

A first course in physics devoted to such topics as vectors, equilibrium, moments, kinematics, gravitation, plane motion, impulse, momentum, rotation, energy, harmonic motion, waves, temperature scales, heat transfer, gas laws and thermodynamics. (Prerequisite: C-402)

Economics (EC-502) [3-0-3]

An introduction to the "science of scarcity" with emphasis on macroeconomics issues. Topics include costs, prices and output for the individual firm; supply and demand in markets; and national income, money supply, inflation, recession, employment and government policies in the national economy.

Microprocessor Hardware and Software Laboratory (MS-501L) [0-2-1]

This laboratory course emphasizes the programming of a microprocessor using assembly language techniques and directly supports the Microprocessor Hardware and Software course. Through software, the student studies the relationship between hardware and software in a microprocessor, including I/O operations. (Prerequisites: DS-401L and concurrent enrollment in MS-501)

Physics I Laboratory (P-502L) [0-2-1]

This course provides experiments related to the Physics I course. The laboratory experiments emphasize measurement principles, error considerations, limits of precision and accuracy, data tabulation and demonstration of physical concepts. Topics include equilibrium, plane motion, collisions, Newton's Law, friction, work and energy, and harmonic motion. (Prerequisite: concurrent enrollment in P-502)

Transform Analysis and Applications Laboratory (TA-501L) [0-2-1]

This laboratory course supports the Transform Analysis and Applications course. Laboratory exercises provide practical experience in designing and building filters to meet specifications and in the use of a computer as a design tool. (Prerequisite: concurrent enrollment in TA-501)

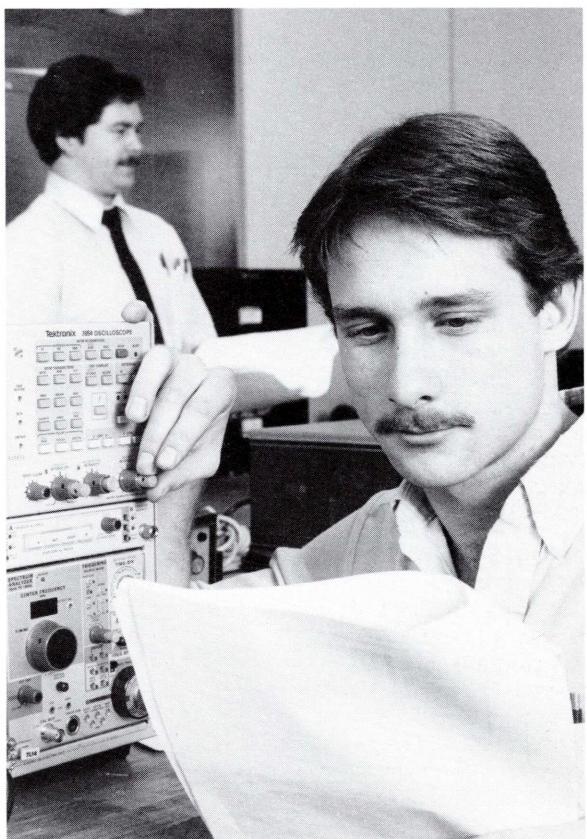
Sixth Trimester

Communication Circuits and Systems (CS-601) [5-0-5]

This course is devoted to basic communication systems at the circuit and subsystem level. Topics include modulation, demodulation, transmitters, receivers and a complete analog communication system. Emphasis is placed on circuits and systems using IC technology. (Prerequisites: ED-402 and DS-401)

Microprocessor Peripherals (MP-601) [4-0-4]

Emphasis is placed on I/O techniques and peripheral devices in this course. Topics include address decoder logic, the IEEE 488 bus, PIAs, memory maps, character generators, troubleshooting, and assembly language techniques related to I/O techniques. This course also provides a comparison of 8-, 16- and 32-bit processor characteristics. (Prerequisite: MS-501)



Public Speaking (PS-603) [4-0-3]

This course relates fundamental speech principles to brief oral presentations designed to build confidence and improve speech delivery. Additional emphasis is placed on awareness of audience, understanding of the communication process, and on analytical methods applied to speech evaluation. (Prerequisite: TW-401)

Physics II (P-602) [3-0-3]

This second physics course covers electric fields, magnetic fields, electromagnetic waves, the nature of light, reflection and refraction, and elements of modern physics. (Prerequisite: P-502)

Communication Circuits and Systems Laboratory (CS-601L) [0-2-1]

This laboratory course supports the Communication Circuits and Systems course. Laboratory exercises provide practical experience in communication circuits and subsystems. Typical circuits are breadboarded, and performance measurements are made with a wide variety of instruments. (Prerequisite: concurrent enrollment in CS-601)

Microprocessor Peripherals Laboratory (MP-601L) [0-2-1]

Laboratory exercises in this course support the Microprocessor Peripherals course. Students study I/O programming techniques and peripheral device interfacing, including keyboards and LED displays. (Prerequisites: MS-501L and concurrent enrollment in MP-601)

Physics II Laboratory (P-602L) [0-2-1]

This second physics laboratory builds on the first laboratory course and directly supports the Physics II course. Laboratory exercises relate to topics in light, electric fields and magnetic fields. (Prerequisites: P-502L and concurrent enrollment in P-602)

Seventh Trimester

Digital Communications (DC-701) [5-0-5]

As the second course in the communications sequence, this course is devoted to digital and data communications techniques. Topics include digital modulation methods, multiplexing methods, bandwidth considerations, baud rates, noise considerations, demodulation methods, interfacing techniques, RS-232, half duplex, full duplex, network architecture, protocols, synchronous systems, asynchronous systems and troubleshooting. (Prerequisite: CS-601)

Control Systems I (CT-702) [4-0-4]

This first course in the control systems sequence provides an introduction to control systems theory. Open- and closed-loop systems are examined, and performance characteristics such as steady state and transient response are analyzed. The course includes analysis of both stepper motor and dc servo motor systems. Mechanical components such as lead screws, valves, actuators and position transducers are also covered. (Prerequisite: TA-501)

Principles of Management (PM-701) [3-0-3]

The role of the manager in modern business organizations is clarified through analysis of the basic managerial functions—planning, organizing, controlling, staffing and supervision. Decision making, work-group dynamics, leadership styles and motivation are featured topics. Case studies are used to critically evaluate organizational problems and opportunities. (Prerequisites: PSY-201 and EC-502)

Pascal Programming (CP-703) [3-0-3]

This course is devoted to the Pascal programming language. The inherent structure of Pascal builds on the BASIC language programming experience of the student. Programming assignments include general data processing applications, mathematically based analysis and controller applications. (Prerequisite: BP-101)

Digital Communications Laboratory (DC-701L) [0-2-1]

This laboratory course directly supports the Digital Communications course. Students gain practical experience by breadboarding digital/data modulation and demodulation circuits. System performance measurements are made, and experience is provided in troubleshooting defective systems. (Prerequisites: CS-601L and concurrent enrollment in DC-701)

Control Systems I Laboratory (CT-702L) [0-2-1]

This laboratory course directly supports the Control Systems I course. Laboratory exercises provide the student with experience in both analog and digital control system fundamentals. Stepper and dc servo motor control systems are covered. (Prerequisite: concurrent enrollment in CT-702)

Pascal Programming Laboratory (CP-703L) [0-2-1]

This laboratory course directly supports the Pascal Programming course. The assignments include such topics as looping and control structures, arrays, recorders, linked lists, procedures and functions. (Prerequisite: concurrent enrollment in CP-703)

Eighth Trimester

Control Systems II (CT-802) [4-0-4]

Building on the first course in the control systems sequence, this second course covers advanced analysis of control systems including Bode plots, root locus and compensation. Also included is an analysis of digital control systems using the z-transform. Microprocessor-based control systems are presented, and the student is introduced to computer-based analysis of control systems. (Prerequisite: CT-702)

Microprocessor Systems I (MS-801) [3-0-3]

In this course the student integrates previous digital course work by designing a microprocessor controller (hardware and software), and by performing a system analysis on a typical microcomputer. Practical methods of troubleshooting and debugging are discussed in detail. (Prerequisite: MP-601)

Contemporary History (CH-801) [3-0-3]

This course traces major developments of the 20th century and their contributions to shaping the modern world. Social, political, economic and technological changes are analyzed. Representative topics include the development of mass production, the Great Depression, the major wars, space technology, civil rights, computers, Watergate, environmental concerns, energy and others. (Prerequisites: E-301 and EC-502)

Contemporary Literature (CL-801) [3-0-3]

This course aims to develop the student's appreciation and understanding of fictional and imaginative works such as short stories, novels and plays. Reading and analysis highlight techniques of language, structure and characterization and the ideas and values that emerge from them. Literary works are also evaluated in relation to their times and in relation to other forms of art. (Prerequisite: E-301)

Career Development (CD-801) [2-0-2]

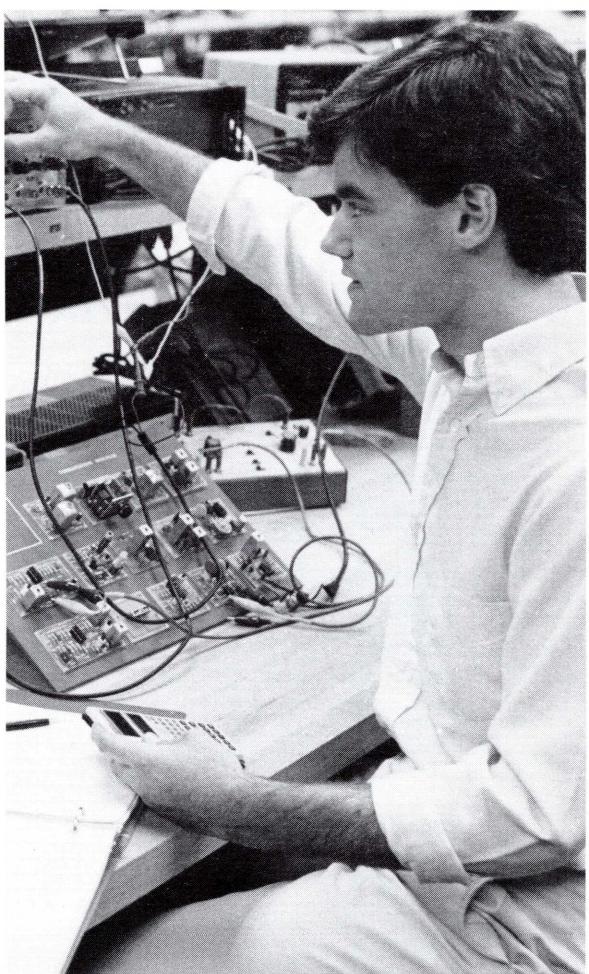
This course introduces students to career-planning strategies and resources that will prepare them for the job-search process and lay the basis for long-term professional growth. Major elements of the course are self-evaluation, knowledge of the electronics field and its requirements, goal setting, company research, a personal marketing plan, preparation of resumes and application letters, and the planning and practice required for effective self-presentation during the job interview. Each student also assembles a portfolio of career development materials. (Prerequisites: PSY-201 and PM-701)

Control Systems II Laboratory (CT-802L) [0-2-1]

This laboratory course directly supports the Control Systems II course. Laboratory exercises provide the student with experience in the analysis, performance measurement and troubleshooting of analog and digital control systems. Special emphasis is placed on microprocessor-based control systems. (Prerequisites: CT-702L and concurrent enrollment in CT-802)

Microprocessor Systems I Laboratory (MS-801L) [0-2-1]

In this course, the student constructs, tests and debugs the microprocessor controller designed in the Microprocessor Systems I course. Documentation is emphasized. (Prerequisite: concurrent enrollment in MS-801)



Ninth Trimester

Advanced Communication Systems (CS-901) [5-0-5]

This third course in the communications sequence is devoted to the systems level. Topics include microwave systems, antenna and transmission lines, optical communication systems, multiplexed systems, satellite systems, wire systems, system performance measurements, and error detection and correction techniques. (Prerequisite: DC-701)

Microprocessor Systems II (MS-901) [3-0-3]

Several microprocessor systems (both hardware and software) are discussed in this course. Topics include diagnostic software, communications software, disk hardware and operating systems, and the fundamental principles of compiler and interpreter operations. Other topics include bit slice systems, advanced memory techniques and practical microprocessor applications. (Prerequisite: MS-801)

Social Issues in Technology (ST-901) [3-0-3]

The relationship between society and technology is investigated through readings, discussions and reports. The course identifies conditions that have promoted technological development and assesses the social, political, environmental, cultural and economic impact of current technology. Issues of control and ethical considerations in uses of technology are explored. The discussions and the preparation of oral and written reports draw together students' technical knowledge and the ideas and skills gained in previous General Education courses. (Prerequisite: CH-801)

Business Operations (OP-901) [3-0-3]

This course is a study of the planning and control techniques used in the financial, production and marketing operations of business. The focus is on realistic evaluation of decisions through analysis of financial statements budgets, production control methods (including computer applications) and marketing approaches. (Prerequisite: PM-701)

Microprocessor Systems II Laboratory (MS-901L) [0-2-1]

In this course, the student must plan, design, build and debug the system interface and software required to accomplish a practical application of a microprocessor assigned by the instructor. Emphasis is placed on planning procedures and documentation. (Prerequisite: concurrent enrollment in MS-901)

Advanced Communication Systems Laboratory (CS-901L) [0-2-1]

This laboratory course directly supports the Advanced Communication Systems course. Laboratory exercises provide the student with experience in complete communication systems including wire, optical and microwave systems. Emphasis is placed on performance measurements and troubleshooting. (Prerequisites: DC-701L and concurrent enrollment in CS-901)



Electronics Technician Course Descriptions

The Electronics Technician program consists of five 15-week trimesters. Upon successful completion of the program, the student is eligible for an Electronics Technician Diploma. The program includes electronics fundamentals, communication systems, computers, and instrumentation and control systems with emphasis on operation and maintenance. The student operates, tests and services many types of equipment in preparation for one of today's most active fields.

DeVry Inc. reserves the right to alter the total number of contact hours listed for reasons including, but not limited to, the following: natural occurrences beyond DeVry's control, holidays, special institutional activity days and registration days.

NOTE: In the three numbers after each course title, the first refers to the hours per week in the classroom, the second to the hours per week in the laboratory and the third to the number of credit hours.

First Trimester

Technical Mathematics I (TM-10) [3-0-3]

This course covers the math topics needed to understand the electronics concepts covered in the Electronics Fundamentals course. Topics include basic mathematical operations involving fractions, decimals, signed numbers and numbers written with powers of ten in scientific or engineering notation; SI metric units; graphics and right triangle problems.

Supplemental Algebra (SA-11) [3-0-0]

This course covers number systems, monomials, polynomials, addition, subtraction, multiplication and division of polynomials, factoring, algebraic fractions, complex fractions, linear equations and radicals.

Electronics Fundamentals (EF-11) [6-0-6]

This beginning course in electronics is devoted to the basic concepts of electricity and electrical circuits, including Ohm's Law, DC series and parallel circuits, power, the generation and nature of AC, basic AC resistive circuits, capacitors, inductors and transformers. Basic power supply and amplifier operation and troubleshooting procedures are also included. (Prerequisite: concurrent enrollment in TM-10)

Electronics Fundamentals Laboratory (EF-11L) [0-4-2]

This laboratory course is intended to demonstrate important concepts studied in the Electronics Fundamentals course, to familiarize students with various devices and circuits, and to develop students' skills in reading schematic diagrams, fabricating simple circuits, using basic test equipment in making circuit measurements and troubleshooting simple circuits and units. (Prerequisites: concurrent enrollment in EF-10 and TM-10)

Digital I (DI-10) [3-0-3]

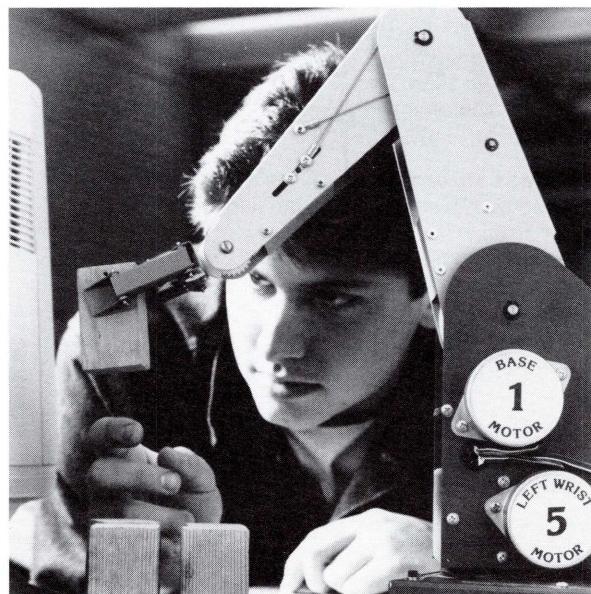
This first course in digital technology introduces the number systems, including binary, decimal and hexadecimal. Basic digital hardware is introduced through Boolean expressions, logic gates, flip-flops and latches.

Digital I Laboratory (DI-10L) [0-2-1]

This course provides practical experience related to pulse and digital circuits. The laboratory exercises are designed to develop familiarity with the digital circuits and subsystems which are the building blocks of computer, control and communication systems. (Prerequisite: concurrent enrollment in DI-10)

Student Orientation (SO-12) [1-0-0]

This is an introduction to DeVry programs and to the personal and academic challenges of college life. The course explores the curriculum and the requirements for graduation, career options, study skills and personal management strategies.



Second Trimester

Technical Mathematics II (TM-20) [3-0-3]

As did Technical Mathematics I, this course also offers coverage of math topics needed for understanding in concurrent electronics courses. Topics include the algebraic operations of factoring binomials, polynomials and equations and calculating power ratios in decibel and dBm units. The use of an electronic calculator for solving basic trigonometric function problems dealing with triangles, sines, cosines and tangents is covered. Also included are solving for triangle sides and hypotenuse lengths and angles, and adding vector quantities, phasors, and in- and out-of-phase components. (Prerequisite: TM-10)

Electronics Applications (EA-20) [4-0-4]

This course provides practical applications for the concepts and devices covered in the electronic fundamentals sequence. Topics such as switches, operational amplifier functions and applications, resonant circuits, filter applications, oscillators and power amplifiers are included. (Prerequisites: EF-10 and concurrent enrollment in TM-20)

Electronics Applications Laboratory (EA-20L) [0-2-1]

In this course, electronic components are used to construct amplifiers, filters, oscillators and power supplies. Typical laboratory test equipment such as digital voltmeters and oscilloscopes is examined and used to test the devices constructed. (Prerequisites: EF-10L and concurrent enrollment in EA-20 and TM-20)

Electronic Devices (ED-20) [4-0-4]

As part of the electronics fundamentals sequence, this course includes such topics as integrated circuits, low- and high-frequency amplifiers, miscellaneous solid-state devices, zener diodes, regulators and amplifier applications. (Prerequisites: EF-10 and concurrent enrollment in TM-20)

Electronic Devices Laboratory (ED-20L) [0-2-1]

This course is a continuation of the Electronics Fundamentals Laboratory course. Students construct devices such as integrated circuits, low- and high-frequency amplifiers, zener diodes and voltage regulators, and examine these devices with standard laboratory test equipment. (Prerequisites: EF-10 and concurrent enrollment in ED-20 and TM-20)

Digital II (DI-20) [3-0-3]

This second course in digital technology covers the timing characteristics of flip-flops and latches in detail, as well as the operation and application of other multivibrators. Electrical and switching characteristics of TTL and CMOS circuits are also discussed. Tri-state and open-collector outputs are also analyzed as they relate to bus structures and other applications. (Prerequisites: DI-10 and concurrent enrollment in EA-20 and ED-20)

Digital II Laboratory (DI-20L) [0-2-1]

In this course, digital circuits are constructed, and their waveforms are analyzed with oscilloscopes and other laboratory test equipment to provide practical experience and further familiarity with the building blocks of current and future electronic devices. (Prerequisites: DI-10L and concurrent enrollment in EA-20, ED-20 and DI-20)

Third Trimester

Communication Skills (CS-32) [3-0-3]

This course aims to strengthen writing skills through an integrated approach based on reading, writing and revision. Students read about technical subjects and write summaries of the key points. The written summaries are then used for a review of grammar principles and for teaching paragraph development. These activities lead into written applications, including a business letter, memorandum, definition paragraph and technical description.

Introduction to Microprocessors (MP-30) [5-0-5]

This introductory course to microprocessor systems begins by covering the basic circuits required for microprocessor operation, including counters, encoders and decoders, triggered registers, multiplexers and demultiplexers, adders, and A/D and D/A converters. Also covered are basic types of memory circuits, memory expansion, control signal timing, microprocessor block diagram components and a programming model. Basic flowchart concepts and software terms and commands are covered, as are certain common programming techniques such as subroutines and various types of interrupts. From short flowcharts, students write programs in mnemonic and machine code forms. Programming tools such as editors and assemblers are also explained. (Prerequisites: ED-20 and DI-20)

Introduction to Microprocessors Laboratory (MP-30L) [0-2-1]

This first microprocessors laboratory course provides exercises to develop familiarity with the basic operation of the microprocessor. The relationship between hardware and system software is also explored through these exercises. (Prerequisites: ED-20, DI-20 and concurrent enrollment in MP-30)

Basic Electronic Communications (BC-30) [4-0-4]

This course covers audio and RF circuits. The introduction to RF principles is provided by coverage of AM transmitter and receiver principles and typical circuits. A complete transceiver is discussed as a typical application of AM principles. Topics such as phase-locked loop circuits and frequency synthesizers are introduced in the discussion of FM systems. FM transmitter and receiver principles and circuits are also included. (Prerequisites: EA-20, ED-20 and TM-20)

Basic Electronic Communications Laboratory (BC-30L) [0-2-1]

This electronic communications laboratory course provides exercises designed to develop familiarity with the basic audio and radio frequency circuits found in modern communication systems. (Prerequisites: EA-20, ED-20, TM-20 and concurrent enrollment in BC-30)

Industrial Control Systems (CT-30) [3-0-3]

This course begins with an introduction to the types of transducers used in industry for monitoring and processing operations, along with their control and amplification systems. Electric motors are also covered, including their characteristics and control circuits. Typical instruments and their applications, stepper motors and multi-phase power systems are also discussed. Other control system topics include various microprocessor control system applications and the elements which make up a control system. (Prerequisites: EA-20, ED-20 and TM-20)

Industrial Control Systems Laboratory (CT-30L) [0-2-1]

This laboratory course provides experience in constructing and testing of basic instrumentation and analog control circuits. (Prerequisites: EA-20, ED-20, TM-20 and concurrent enrollment in CT-30)

Fourth Trimester

Career Development (CD-42) [3-0-3]

This course focuses on career-planning strategies designed to maximize employment potential and lay the basis for long-term professional growth. Through a series of learning experiences and activities, students develop their knowledge of the business world and the electronics industry, of research and problem-solving techniques, and of practical job-search methods. Further attention is devoted to exploration of career interests and values, development of positive attitudes, improvement of communication and interpersonal skills, and preparation for job interviews. Course work is integrated through a final project in which students assemble a portfolio of materials aimed at helping them research their employment and career objectives.

Microprocessor Systems (MS-40) [5-0-5]

In this course students learn programming techniques that include priority interrupts for the type of microprocessor used in the lab, and short input, output or loop routines written in a high-level computer language. Students initiate flowcharts and programs and contrast low- and high-level flowcharts. The contents of the microprocessor registers are examined after each instruction is executed in short programs that include data transfers, arithmetic/logic, branch and I/O instructions and stack operations, and in short diagnostic routines that test memory and I/O ports. Short routines in assembly language will be converted to machine code. The instruction set and addressing modes for a microcomputer are also examined. (Prerequisite: MP-30)

Microprocessor Systems Laboratory (MS-40L) [0-2-1]

In this lab, program branching techniques are used, and diagnostic troubleshooting techniques are applied as step-by-step program advances are checked through a microprocessor's registers, memory and I/O ports. (Prerequisites: MP-30L and concurrent enrollment in MS-40)

Analog Communication Systems (AC-40) [3-0-3]

This second course in electronic communications begins by further examination of receiver types and operations initially examined in the Basic Electronic Communications course. Transmission lines, antennas, mobile- and base-communication installations and testing and troubleshooting procedures complete the list of topics included in this course. (Prerequisite: BC-30)

Analog Communication Systems

Laboratory (AC-40L) [0-2-1]

During this laboratory course students investigate the operation and troubleshooting of various transmission systems and antennas. (Prerequisites: BC-30 and concurrent enrollment in AC-40)

Digital/Data Communication Systems

(DD-40) [4-0-4]

This course is devoted to digital and data communication systems techniques. Topics include network configurations, network protocols, synchronous and asynchronous communications, multi-channel communications, UART operations, modem operation and troubleshooting. Pulse communication circuits are also discussed, along with pulse-code and pulse-width modulation. (Prerequisites: BC-30 and MP-30)

Digital/Data Communication Systems

Laboratory (DD-40L) [0-2-1]

This laboratory course supports the Digital/Data Communication Systems course. Practical experience includes taking measurements on and troubleshooting digital/data modulation and demodulation circuits. (Prerequisites: MP-30, BC-30 and concurrent enrollment in DD-40)

Fifth Trimester

Technical Communication (TN-50) [3-0-3]

This course combines further development of writing skills with oral communication activities. The common elements of oral and written communication (for example, planning, organization and development of material) are explained. In addition, the special requirements of oral communication (for example, use of voice, gestures, eye contact, etc.) are demonstrated and practiced. Speech activities build on active listening practice and stress visual aids as key elements. (Prerequisite: CD-42)

Microprocessor Applications (MA-50) [5-0-5]

This third microprocessor course defines an operating system (CP/M) and its software and hardware requirements and shows how compatibility is achieved among its separate components. The functions of the interconnecting data, address, control and status busses of a microcomputer are explained along with the necessary memory and I/O accessing techniques and devices. From the timing diagrams and programming cards of a microprocessor, students will identify when specific instructions are executed and list the sequence of events that occur during each read/write operation. Loading

effects on busses and buffers are covered, as are keyboard interfacing techniques and the ranges of usable addresses for each memory device. Monochrome and RGB video monitor operation and system interconnections are covered, along with those of hard and floppy disk drive systems. Formatting and care of disks are covered. The operation of video generators and dot matrix character generators is also covered. (Prerequisite: MS-40)

Microprocessor Applications Laboratory (MA-50L) [0-2-1]

Laboratory exercises in this course cover microprocessor system interface circuits and system peripherals. Good troubleshooting skills are developed, and both hardware and software techniques are studied. (Prerequisites: MS-40L and concurrent enrollment in MA-50)

Advanced Communication Systems (AC-50) [4-0-4]

This fourth course in the communication systems sequence is devoted to the systems level. Topics include microwaves, cellular radio and telephone PBX systems, and packet-switching communication networks. Operation of and troubleshooting procedures for these systems are also covered. (Prerequisites: DD-40 and MS-40)

Advanced Communication Systems Laboratory (AC-50L) [0-2-1]

This course provides exercises at the systems level in microwave, cellular radio and telephone systems. The emphasis is on system operation and troubleshooting procedures. (Prerequisites: MS-40, DD-40 and concurrent enrollment in AC-50)

Advanced Industrial Control Systems (CT-50) [3-0-3]

As a continuation of the industrial control systems sequence, this course includes such topics as SCR switching circuits, switching power supplies, ladder-logic relay circuits, sequential time-delay circuits, PID controllers and microprocessor control systems. The topics discussed in this course are covered using system operation and system troubleshooting approaches. (Prerequisites: MS-40 and CT-30)

Advanced Industrial Control Systems Laboratory (CT-50L) [0-2-1]

This laboratory course provides experience in working with microprocessor control systems including switching power supplies, PID controllers and sequential time-delay circuits. System operation and troubleshooting procedures are stressed. (Prerequisites: CT-30, MS-40 and concurrent enrollment in CT-50)

Computer Information Systems Course Descriptions

The student is eligible for a Bachelor of Science Degree in Computer Information Systems after successfully completing the eight-trimester (a trimester is 15 weeks in length) curriculum. The program is intended to provide the student with the background needed to function in business applications of computer information systems.

The graduate of the six-trimester program is eligible to receive an Associate of Applied Science Degree in Computer Information Systems.

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NOTE: In the three numbers after each course title, the first refers to the hours per week in the classroom, the second to the hours per week in the laboratory and the third to the number of credit hours.

First Trimester

Introduction to Data Processing (IS-100) [3-0-3]

This course introduces the student to the principles of computer information systems. Topics discussed include types of files, operating systems, hardware, software, history of data processing, job functions, documentation for systems and programs, programming languages, security and control issues, social issues, distributed data processing and management of information systems. Career paths available for Computer Information Systems graduates are also examined.

Pascal Programming (CP-100) [5-0-5]

The student is introduced to structured programming through the use of Pascal. Pascal provides the foundation of problem-solving techniques to build knowledge and understanding of advanced languages. The principles of a general form of a Pascal program, including Boolean algebra, control statements, procedures and functions, data types, arrays, dynamic data structures, recursion, design logic and flow charting, are presented. (Prerequisite: concurrent enrollment in CP-100L)

Applied Algebra (DM-100) [4-0-4]

Mathematics develops problem-solving ability and provides a foundation for analysis of business problems. Topics include fundamental rules of algebra, graphing, problem-solving techniques, real numbers, mathematical operations, concepts of variables and the use of algebra as applied to business applications.

Business Organization (CSS-100) [3-0-3]

This course provides an introduction to the internal and external environments of business. Market forces, organizational structure, and the functions, goals, methods and tools of business enterprise are examined.

Pascal Laboratory (CP-100L) [0-4-2]

Theory is brought into practice through a variety of Pascal programming assignments. Such assignments concentrate on the key elements of structured problem solving. Students are introduced to the case study approach. (Prerequisite: concurrent enrollment in CP-100)

Student Orientation (SO-101) [1-0-0]

This is an introduction to DeVry programs and to the personal and academic challenges of college life. The course explores the curriculum and the requirements for graduation, career options, study skills and personal management strategies.

Second Trimester

COBOL I (CP-110) [4-0-4]

COBOL is the major business-oriented programming language. Students learn to apply top-down modular programming techniques, develop and document a test plan that verifies the logic of an applications program, including fatal and nonfatal errors, formulate and present a structured walk-through, and document a given application program including file layouts, record layouts, narratives, and appropriate methods of external logic documentation (flow diagrams, HIPO charts, pseudo-code, decision tables, etc.). (Prerequisites: CP-100 and concurrent enrollment in CP-110L)

Data Processing Math (DM-110) [4-0-4]

Specific mathematics topics relating to data processing are presented in this course. Topics include numeration systems, manipulation of numeration systems, scientific notation, set theory, logic and Boolean algebra as applied to business. (Prerequisite: DM-100)

Introduction to Accounting (AC-100) [4-0-4]

This course is an introduction to basic accounting principles. The student is familiarized with basic principles by studying T-accounts, balance sheets, journal entries, general journals, A/P and A/R ledgers, trial balance, profit and loss, income statements, and purchasing and sales journals.

English I (CCS-100) [3-0-3]

Students are introduced to the elements of composition through analysis of essays, articles and other written works. The readings are used as models for the development through writing practice of basic composition skills. Written assignments incorporate the requirements of sentence-level English into paragraphs, short essays and informal reports.

COBOL Laboratory I (CP-110L) [0-4-2]

Through case studies and other assignments, students gain proficiency in writing and testing structured COBOL programs. Documentation techniques are stressed, and students learn to debug programs. (Prerequisite: concurrent enrollment in CP-110)

Third Trimester

COBOL II (CP-220) [4-0-4]

This second course in the COBOL sequence introduces advanced concepts such as file handling and builds on prior knowledge. The student learns to design, document, code and test COBOL programs using structured techniques to solve a variety of advanced applications, including the use of sequential and random access files. The student writes a set of user instructions in narrative form and compares and contrasts various file organization and handling techniques given a specific business application. (Prerequisites: CP-110 and concurrent enrollment in CP-220L)

Systems Analysis I (SY-200) [4-0-4]

The case study method is used to orient the student to information as a corporate resource, the life cycle of computer-based business systems, the role of the systems analyst and business as an information system. The steps involved in a systems analysis are examined and practiced providing the student with knowledge of the life-cycle process. (Prerequisite: CP-110)

Financial Accounting (AC-210) [4-0-4]

This second course in the accounting sequence applies the principles of accounting to specific financial accounting systems, proprietorships, partnerships and corporations. Topics include transactions pertaining to the sale of stock, stock splits, dividends, cash dividends and the purchase and resale of treasury stock. Students prepare a statement of change of financial position. (Prerequisite: AC-100)

English II (CCS-210) [3-0-3]

Analysis and critical evaluation of essays, articles and other writings are used to focus attention on style, organization and development strategies. In writing assignments, emphasis is placed on formulation and development of the thesis in essays that are edited and revised for maximum effectiveness. A library research paper is included. (Prerequisite: CCS-100)

COBOL Laboratory II (CP-220L) [0-4-2]

Given business application problems, the student will generate multiple reports and files and analyze, design, document, code and test using sequential files, random access and other current methods. Programming assignments using the case study approach stress structured principles and utilize advanced techniques including sequential and random access files. Assignments also include multiple reports and stress the analysis, design, document, code and test sequence. (Prerequisite: concurrent enrollment in CP-220)

Fourth Trimester

Systems Analysis II (SY-210) [4-0-4]

An understanding of structured system development procedures using data-driven and data-flow approaches is presented. Case studies provide a framework for the investigation of structured design tools, project-planning techniques and control tools. (Prerequisite: SY-200)

Operating Systems and JCL (IS-210) [4-0-4]

Through analysis of various operating systems, students learn to: write JCL utilities to sort, merge and create files; describe the features of PROCs, libraries, data sets, cataloging, updating, compile, link, edit and go; and understand operating system configurations and recovery techniques. Major vendors' operating systems are compared. (Prerequisites: CP-110 and concurrent enrollment in IS-210L)

Introduction to Statistics (DM-220) [3-0-3]

This course provides an introduction to statistics. Probability distribution, quantitative measurement, descriptive statistics, sampling methods, procedures for hypothesis testing, regression methods and correlation analysis as used in the business environment are presented. (Prerequisite: DM-110)

Business Writing (CCS-220) [3-0-3]

This course builds on general composition principles to explore the requirements of common types of business-related writing. Audience analysis is a key element in determining the approaches and formats used in the

proposal, the memorandum, the progress report, documentation, user instructions and other applications. (Prerequisite: CCS-210)

Psychology (CSS-210) [3-0-3]

From a consideration of the aspects of individual functioning, such as personality, motivation, perception and creativity, this course moves to a study of group dynamics in an organizational setting. Communication, leadership, decision making and other topics are included.

JCL Laboratory (IS-210L) [0-2-1]

Assignments are focused on job streams utilizing major utilities such as IEBGENER, IEBPTPCH, IEBDG, IEHLIST, IEBCOMPR and Sort/Merge to read and write multiple files. (Prerequisite: concurrent enrollment in IS-210)

Fifth Trimester

BAL Programming (CP-330) [4-0-4]

A course in assembly language provides insight into the detailed operation of a computer system. Topics and applications are chosen to enhance the student's ability to debug COBOL programs and include program shell and macros, comparing, branching, decimal arithmetic, editing, subroutines, bit manipulation, RR and RX instructions, looping and explicit addressing. (Prerequisites: CP-100 and concurrent enrollment in CP-330L)

Small Business Computer Systems (IS-320) [4-0-4]

Using a microprocessor-based system, the student analyzes the special requirements, hardware and software, of small business systems. Commercial software packages are examined, including word processors, financial spreadsheets and business graphics. (Prerequisite: IS-100)

Public Speaking (CCS-330) [4-0-3]

A series of short oral presentations provides applications for the study of basic speech principles. Attention is given to audience analysis, organization, language, timing and the nonverbal elements of speech. (Prerequisite: CCS-220)

Economics (CSS-320) [4-0-4]

This introduction to the social science of economics examines the basic assumptions, methods and models of the discipline. Essential microeconomic concepts such as demand, supply and market equilibrium provide a basis for analysis of the macroeconomy. GNP, money, business cycles, government policies, foreign trade and other topics are evaluated. (Prerequisite: CSS-100)

BAL Laboratory (CP-330L) [0-4-2]

Programming assignments utilize the concepts of assembly language for manipulation of data from file to file and print, updating, editing and basic business formulas. (Prerequisite: concurrent enrollment in CP-330)

Sixth Trimester

Applications/Maintenance Programming (CP-340) [4-0-4]

Maintenance and updating of programs is a major business activity. In this course the student learns maintenance techniques including debugging programs, designing, coding, testing, updating to master files, running applications, making modifications and changes, and writing operating procedures to run an application. (Prerequisites: CP-220 and concurrent enrollment in CP-340L)

Controls & Security (IS-330) [4-0-4]

This course is designed to provide the student with a fundamental understanding of computer audit, controls and security. Different types of audit approaches are discussed using auditing around, through and with the computer. Programs are written in report writer, COBOL, utilities and a generalized audit retrieval package to interrogate files. Preparation and presentations of audits, working papers, findings and reports are utilized with case studies. (Prerequisites: CP-220, SY-210 and concurrent enrollment in IS-330L)

Managerial Accounting (AC-320) [4-0-4]

Managerial accounting provides the student with experience in real life business operations. Topics include standard cost systems, budgeting, break-even analysis and corporate taxes. Comparisons are drawn between service, retail and manufacturing organizations. Manual and automatic accounting systems are compared and contrasted. (Prerequisite: AC-210)

Management (CSS-330) [3-0-3]

The role of the manager is explored through analysis of the managerial functions—planning, organizing, directing, controlling and coordinating. Other important topics include decision making, quantitative methods and tools, change management and motivational techniques. (Prerequisites: CSS-210 and CSS-320)

Applications/Maintenance Programming Laboratory (CP-340L) [0-2-1]

The student is assigned business application case studies and writes the necessary code to make the programs operational using standard COBOL programming principles. (Prerequisite: concurrent enrollment in CP-340)

Controls & Security Laboratory (IS-330L) [0-2-1]

Case studies are assigned demonstrating the use of utilities, COBOL, report writers and generalized audit retrieval packages as tools to interrogate files. (Prerequisite: concurrent enrollment in IS-330)

Seventh Trimester

Data Structures (SY-420) [4-0-4]

This course introduces the student to the basic concepts of data structures including sets, stacks, queues, pointers, keys, general trees, binary trees and B trees, and describing the allocation of memory management. (Prerequisites: CP-330, CP-340 and concurrent enrollment in SY-420L)

On-Line Systems (IS-440) [4-0-4]

The student is introduced to interactive and on-line computer system techniques. The concepts of on-line processing, screen writing techniques, manipulation, control, design and format are stressed. Menu-driven applications, preparing user manuals, updating files and back-up files for applications are also covered. (Prerequisites: CP-340 and concurrent enrollment in IS-440L)

Management Information Systems (IS-450) [3-0-3]

This capstone course investigates the highest level of information support systems which provide quantitative-based information to aid managers in the decision-making process. Theoretical concepts are applied to real world applications with an analysis of examples from specific organizations. (Prerequisite: CP-340)

Career Development (CSS-440) [2-0-2]

This course introduces students to career-planning strategies and resources that will prepare them for the job-search process and lay the basis for long-term professional growth. Major elements of the course are self evaluation, knowledge of the data processing field and its requirements, goal setting, company research, a personal marketing plan, preparation of resumes and application letters, and the planning and practice required for effective self-presentation during the job interview. Each student also assembles a portfolio of career-development materials. (Prerequisites: CSS-100 and CSS-210)

Contemporary Literature (CLA-400) [3-0-3]

Stories, novels, plays and poems are studied for both their content and as examples of literary technique. The relationship of literature to other kinds of art and to a historical and cultural setting is explored. Students are encouraged to develop personally meaningful responses to works of art through close analysis, discussion, research and written reports. (Prerequisite: CCS-210)

Data Structures Laboratory (SY-420L) [0-2-1]

Assignments stress file manipulation and the principles of data bases. Basic structures such as binary trees and memory management techniques are stressed. (Prerequisite: concurrent enrollment in SY-420)

On-Line Systems Laboratory (IS-440L) [0-2-1]

Case studies provide the basis for assignments which include screen writing, input control and menu-driven applications. (Prerequisite: concurrent enrollment in IS-440)

Eighth Trimester

Senior Project (SY-430) [3-0-3]

Through a group-oriented project the student demonstrates and applies the mastered skills and knowledge including problem-solving techniques, the ability to work as a team member and project management. The project, either a case study or outside assignment, provides real world experience integrating the aspects of systems analysis, programming, testing, debugging, documentation and user interfacing. (Prerequisites: senior status, IS-450 and concurrent enrollment in IS-470, IS-470L and IS-460)

Distributed Data Processing (IS-460) [4-0-4]

This course provides an introduction to the basic concepts of a network. Students are introduced to centralized and decentralized operations, strengths and weaknesses of telecommunications, protocols, hardware configuration, multi-drop and polling, and techniques used for up-loading and down-loading files in a distributed system. (Prerequisite: senior status)

Data Base Management Systems (IS-470) [5-0-5]

DBMS concepts are examined as applied to networks, file management, data base structures, disaster recovery and back-up procedures. (Prerequisites: SY-420 and concurrent enrollment in IS-470L)

Evening School— Digital Electronics Technician Course Descriptions

Perspectives on Technology

(CLA-410) [4-0-4]

Major historical developments of the 20th century provide a framework for evaluation of the social impact of technological innovations. The relationship between technology and society is explored through readings, case studies, research, discussion and reports.
(Prerequisites: CCS-210 and CSS-320)

Data Base Management Systems Laboratory

(IS-470L) [0-4-2]

IMS provides the base for a series of assignments which guides the student through the process of designing, implementing and maintaining a data base. Techniques used to access, update and delete in a data base environment are stressed. (Prerequisite: concurrent enrollment in IS-470)



The Digital Electronics Technician program provides a thorough background in fundamental electronics and applied mathematics with specialized experience in digital technology. Microcomputers and systems, basic computer techniques, digital circuits and subsystems are covered in detail. A thorough, fundamental electronics sequence provides a base for the advanced digital work as well as for further study in other specialty areas. Laboratory work supports theory and provides practical, hands-on experience. The Digital Electronics Technician program consists of six, 15-week terms (90 weeks), with 12 hours of instruction per week. After successful completion of this program, the student is eligible for a diploma.

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NOTE: In the three numbers after each course title, the first refers to the hours per week in the classroom, the second to the hours per week in the laboratory and the third to the number of credit hours. Total contact (clock) hours are based on instruction and examination periods. Minimum contact hours exclude registration days and holidays.

First Trimester

Electronics Fundamentals I (EFN-12) [8-0-8]

This beginning course in electricity is devoted to basic concepts of electricity and electrical circuits, including Ohm's Law, DC series and parallel circuits, power, the generation and nature of AC, basic AC resistive circuits, capacitors, inductors and transformers.

Electronics Laboratory I (ELN-12) [0-4-2]

The electronics laboratory course supports the theory presented in the first course in electronics fundamentals. The laboratory exercises are designed to develop skill in the reading of schematic diagrams, the use of basic test equipment and troubleshooting simple circuits. (Prerequisite: concurrent enrollment in EFN-12)

Second Trimester

Electronics Fundamentals II (EFN-22) [8-0-8]

This second course in fundamental electronics is devoted to a study of solid state devices including semiconductor diodes, junction transistors and field-effect

transistors. The application of basic solid state devices to power supplies is also covered. Additional work in AC circuit theory includes frequency effects in RLC circuits. AC power, impedance matching, time constants and passive waveshaping are also covered. (Prerequisite: EFN-12)

Electronics Laboratory II (ELN-22) [0-4-2]

Laboratory exercises are designed to further develop skill in reading schematic diagrams as well as breadboarding electronic circuits. The use of test equipment, especially the oscilloscope, in making circuit measurements is emphasized. (Prerequisites: ELN-12 and concurrent enrollment in EFN-22)

Third Trimester

Electronics Fundamentals III (EFN-32) [8-0-8]

The last course in fundamental electronics begins with coverage of parallel AC circuits, resonance, double-tuned circuits, filters and impedance-matching techniques. Additional course topics include basic amplifiers, audio-frequency amplifiers, power supplies, regulators, RF amplifiers, RF oscillators and crystal oscillators. (Prerequisite: EFN-22)

Electronics Laboratory III (ELN-32) [0-4-2]

Laboratory exercises include additional work with the oscilloscope and, in particular, its application to amplifier and oscillator circuits. Both low- and high-frequency techniques are analyzed, and measurement and analysis techniques are stressed. Troubleshooting procedures are also detailed. (Prerequisites: ELN-22 and concurrent enrollment in EFN-32)

Fourth Trimester

Linear and Digital Circuits (LDN-42) [8-0-8]

This first course in the digital specialty area includes an introduction to linear integrated circuits, number systems, waveshaping circuits, Boolean algebra, logic gates, digital displays, multiplexers and demultiplexers. A thorough foundation is provided for further courses in computer and microcomputer technology. The interrelationship and use of linear circuits and digital systems is also covered. (Prerequisite: EFN-32)

Electronics Laboratory IV (ELN-42) [0-4-2]

The laboratory work includes circuit construction and analysis of both linear and digital circuits. The use of the oscilloscope to analyze waveshaping and the action of the digital circuits is covered in detail. The exercises are intended to develop familiarity with digital and analog circuits which are the building blocks of computers and other digital systems. The basic skills necessary to efficiently and effectively test, troubleshoot and maintain a wide range of analog and digital systems are developed. (Prerequisites: ELN-32 and concurrent enrollment in LDN-42)

Fifth Trimester

Digital Computers (DCN-52) [8-0-8]

The second course in digital technology is devoted to computer hardware and peripherals. The use of basic subsystems including counters, arithmetic circuitry, analog-to-digital converters, memories and similar circuitry is pulled together to form a complete computer system. The flow of the data within the computer during program execution is covered in detail. (Prerequisite: LDN-42)

Electronics Laboratory V (ELN-52) [0-4-2]

This laboratory course provides practical experience in the testing and maintenance of digital computer circuits and systems. Basic troubleshooting skills are practiced. (Prerequisites: ELN-42 and concurrent enrollment in DCN-52)

Sixth Trimester

Microcomputer Systems (MSN-62) [8-0-8]

Microprocessors, including theory of operation, architecture, machine and assembly language programming and interfacing, are covered in this course. The student begins by programming the computer in machine language. Later in the course, the hardware and software necessary to control the outside world are discussed. An introduction to BASIC language programming is also provided. (Prerequisite: DCN-52)

Electronics Laboratory VI (ELN-62) [0-4-2]

This laboratory course provides practical experience related to microprocessor systems. Exercises cover basic system architecture, programming and interfacing techniques. Typical real world examples are covered from both the hardware and software standpoints. (Prerequisites: ELN-52 and concurrent enrollment in MSN-62)

Air Force ROTC

The curriculum of the Air Force ROTC program provides professional education to future officers. The last two years of the program, or the Professional Officer Course (POC), are available to students from DeVry Institute of Technology. Courses are not offered during the summer trimester. In the first year, the POC focuses on leadership, professionalism, Air Force management theory and practice, and communication skills. The final year is devoted to a study of a broad range of civil-military relations and the environment in which U.S. defense policy is formulated. The POC courses must be started when there are four trimesters of work remaining, excluding the summer session. A student's commissioning as a Second Lieutenant in the United States Air Force will coincide with the awarding of the baccalaureate degree. Students applying for the program register in the same manner and at the same time as they register for their other courses. The courses are taught at the Ohio State University campus by faculty members who are Air Force officers. Additional information can be obtained by contacting the Department of Aerospace Studies at Ohio State (614) 422-5441 or the Dean of Students at DeVry.

AFROTC Scholarships

Qualified students may apply for two-year Air Force ROTC scholarships. Each scholarship provides for full payment of tuition and fees, textbook reimbursement, not to exceed \$114 per trimester, and includes a tax-free allowance of \$100 each month. Students enrolled in the Professional Officer Course receive \$100 a month tax-free, regardless of their scholarship status. Further information can be obtained by contacting the Department of Aerospace Studies at Ohio State (614) 422-5441.

AFROTC Course Descriptions

Concepts of Air Force Leadership (AF-501) [3-1-3]

This course emphasizes the individual as a leader in the Air Force environment. Individual motivation and behavioral processes, communication and group dynamics are covered to provide a foundation for the development of the junior officer's professional skills.

Concepts of Air Force Management (AF-601) [3-1-3]

A study of organizational and personal values, management of forces in change, organizational power, politics, managerial strategy and tactics; administrative processes also are discussed. Actual Air Force cases are used to enhance the learning and communication processes.

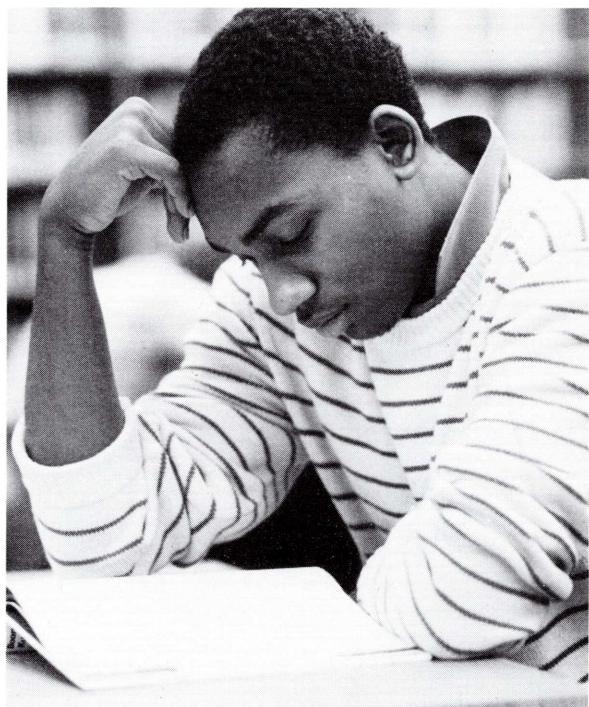
National Security Forces in Contemporary American Society I (AF-801) [3-1-3]

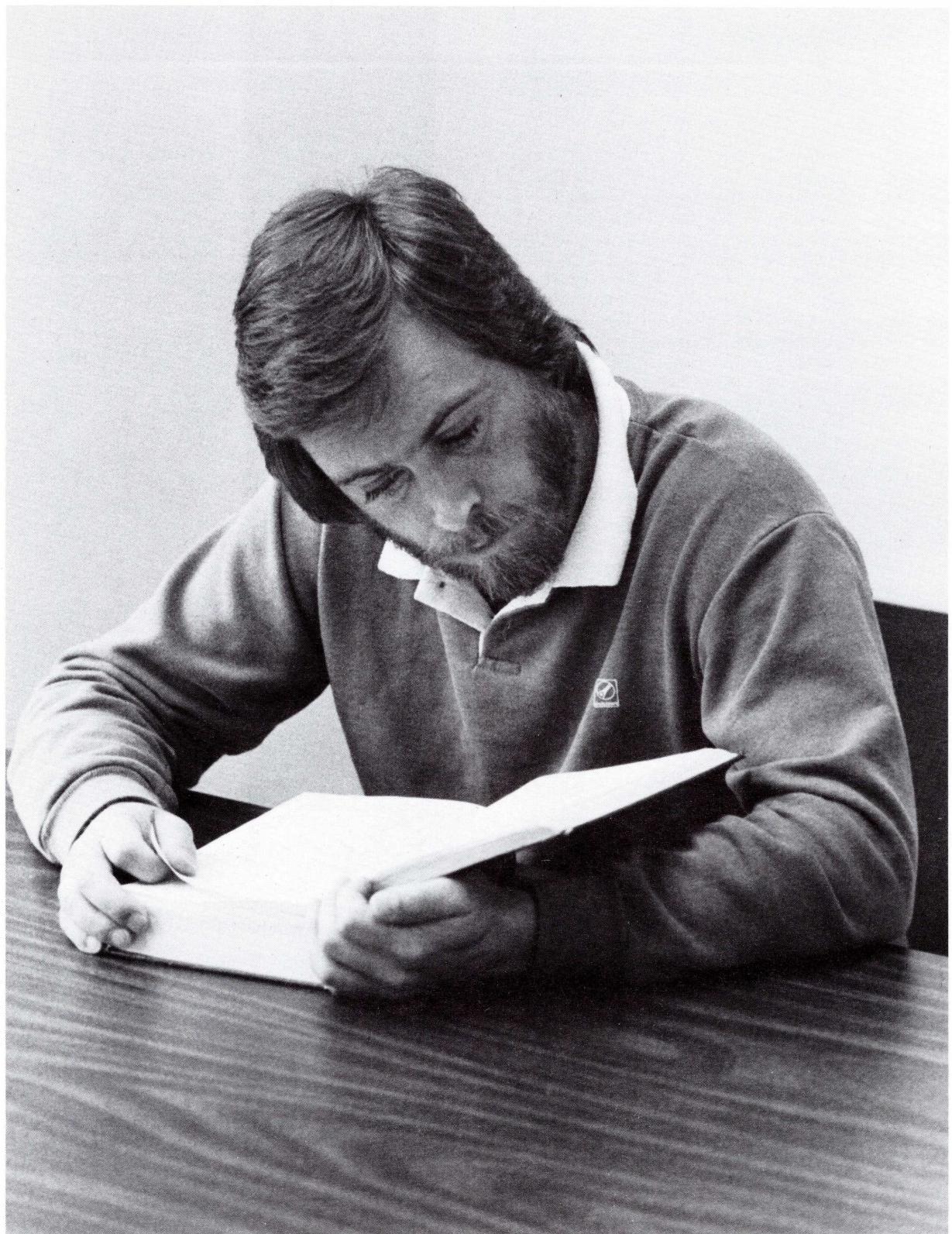
Focuses on the broad range of civil-military relations and the environmental context in which United States defense policy is formulated and effected. Communication skills are considered as well as the role of the leader-manager in a democratic society, societal attitudes toward the Armed Forces and formulation of defense strategy.

National Security Forces in Contemporary American Society II (AF-901) [3-1-3]

Requisites for national security forces, political, social and economic constraints on the defense structure, and the impact of technological and international developments on the overall policy-making process are presented. Includes a review of the military justice system.

NOTE: One hour Leadership Laboratory is required as part of each Aerospace Studies Course. Course offerings and course numbering may vary depending on student status and trimesters remaining until graduation.





Army ROTC

A unit of the Reserve Officers Training Corps was established at DeVry on January 4, 1982, with a goal to qualify men and women for positions of leadership and management in the Armed Forces. By participating in the ROTC program, a student may earn a commission as a Second Lieutenant in the Regular Army, the United States Army Reserve, or the Army National Guard, while pursuing his/her degree. A general military service curriculum is offered which qualifies a cadet for assignment to any one of the branches of the Army. Students applying for the program register in the same manner and at the same time as they register for their other courses.

Army ROTC offers:

- a minimum of \$2,000 in scholarship money to each cadet during the advanced program;
- a commission as a Second Lieutenant in the United States Army;
- a job opportunity with a starting salary and allowances of over \$19,000 per year or an opportunity to serve in the Army Reserves or National Guard;
- extensive leadership and management courses which are applicable to both civilian industry and military service;
- an opportunity to participate in such confidence-building activities as orientation, mountaineering, marksmanship and special warfare activities.

Scholarships

Three- and two-year Army ROTC scholarships are available on a competitive basis. DeVry students who are already participating in the ROTC program or who are eligible for enrollment into ROTC may apply. The scholarship will pay for tuition and lab fees. Students receive a book allowance and a \$100 per month (tax-free) stipend. All nonscholarship cadets enrolled in the advanced ROTC program receive scholarship money or \$100 per month during their enrollment in the program.

Requirements for Enrollment

Any student who is physically qualified, and not already holding a commission in any of the Armed Forces, may enroll in Basic Military Science. Those who meet the above qualifications and have prior military experience or training in the Armed Forces, college or junior ROTC in high school, may take advantage of previous military courses by consulting the Military Science Department when matriculating. Entrance into the Advanced Course (300 or 400 level) is based upon the following:

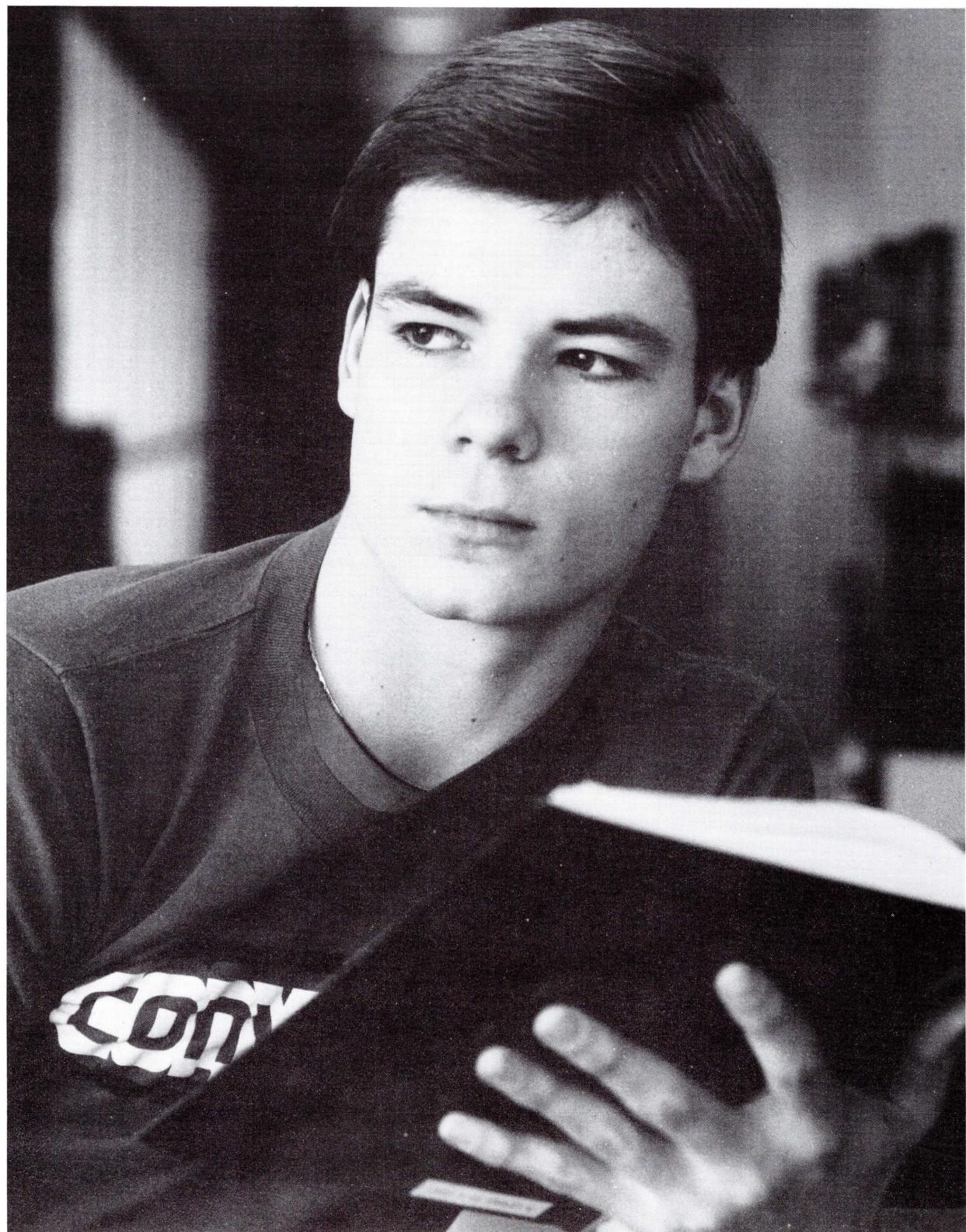
- satisfactory completion of the Basic Course, Basic Camp or advanced placement due to prior military service or high school ROTC;
- successful completion of any Army physical examination;
- execution of appropriate loyalty statements and contractual agreements;
- satisfactory completion of the appropriate screening test;
- selection by the Director of Military Sciences.

Obligations

A student incurs no obligation to the military by participating in Basic Military Science. The course offers a cadet the opportunity to evaluate the prospect of military service and also to qualify for the advanced program. When a cadet enters the advanced program he/she is obligated to accept a commission as a Second Lieutenant in the United States Army Reserve (USAR) upon graduation from a Bachelor's Degree program. There are two choices generally available to the cadet at graduation:

1. Reserve Forces Duty (National Guard or United States Army Reserve)—the cadet may enter the service for approximately 90 days to attend a basic branch school and serve in the Reserves for a period of approximately eight years while pursuing a civilian career. This option may be selected by the cadet and guaranteed prior to entrance into the advanced program.
2. Active Duty—cadets selected for active duty must serve three years on active duty and complete their obligation by staying in the Reserves for five years.
 - a. If desired, Army Reserve Officers may remain on active duty for a military career in an indefinite status.
 - b. Scholarship students will serve four years on active duty as Army Reserve Officers and/or have the option to request a regular army commission.
 - c. Early commissioning into the National Guard and Army Reserve is available to those who have completed all ROTC requirements prior to completion of graduation requirements.

Additional information can be obtained by contacting Army ROTC (614) 224-6237, ext. 222.



ROTC Course Descriptions

Military Science (MS-1)

Introduction to Military Establishment

(MILS-111) [1-0-1]

A comprehensive study of the organization, mission and role of the Military Establishment, the U.S. Army and the ROTC program. The course will also include a survey of the basic fundamentals of first aid.

Military Training: Weaponry/Navigation

(MILS-112) [1-0-1]

A study of military maps and land navigation techniques including terrain evaluation, scale and distance, direction and location. Examination of basic leadership skills and fundamentals of rifle marksmanship.

Military Science II (MS-II)

National Security Policy (MILS-211) [1-0-1]

Survey of military history from World War II to present. Discuss the national security policies of the U.S. and the U.S.S.R. and the use of nuclear, biological and chemical weapons on the modern battlefield.

American Military History (MILS-212) [1-0-1]

Introduction to development of tactics and strategy in American military history illustrated by selected campaigns and battles from the Colonial Period to World War I. Special emphasis on organization, technology and commanders.

Military Science III (MS-III)

Military Leadership (MILS-311) [2-0-2]

A study of behavioral patterns and managerial methods applied to the military environment. The course will also include a survey of the basic soldier skills and radio/wire communications. (Prerequisite: MILS-111 through MILS 212 or permission of instructor)

Military Tactics and Special Operations

(MILS-312) [2-0-2]

An in-depth review of map reading to include practical exercises in a field environment. Comprehensive instruction in small unit tactics to include: analyzing terrain, camouflage, operation orders, movement techniques, patrolling and the preparation for combat. (Prerequisite: MILS-311 or permission of instructor)

Military Science IV (MS-IV)

Military Operations, Management and

Logistics (MILS-411) [3-0-3]

The study of Army organizations and operations to include managerial aspects of command and staff. Emphasis on conduct of effective training, communication skills and resource management. (Prerequisite: MILS-312 or permission of the instructor)

Administration, Military Justice and Professional Ethics (MILS-412) [3-0-3]

Study of personnel, administrative management and military justice. Additional emphasis on the development of professional ethics for the military officer. (Prerequisite: MILS-411 or permission of instructor)

NOTE: MILS-111, MILS-112, MILS-211 and MILS-212 are taught on the campus. All other Military Science classes are taught at Franklin University. Both day and evening classes are available. For additional information, call (614) 224-6237, ext. 222/221.



Refund Schedules

As of March 3, 1986 refunds of trimester tuition will be based upon the following schedules, or as required by applicable federal and state laws and regulations, if more favorable to the student.

Day Students with Standard Schedules

Week of Termination	Term of Attendance and Amount of Tuition Refundable		
	1st Term	2nd Term	Subsequent Terms*
1st week	\$1660	\$1452	\$1369
2nd week	993	968	913
3rd week	497	910	763
4th week	418	910	307
5th-8th weeks	418	910	307
9th-12th weeks	418	307	307
13th-15th weeks	-0-	-0-	-0-

*For students terminating in the 4th through 8th weeks of the 5th or 8th trimester of the Electronics Engineering Technology program and the 4th through 8th weeks of the 5th trimester of the Computer Information Systems program, the refund will be \$763.

For day students carrying less than 12 credit hours, the tuition refunded will be in accordance with the above chart, but reduced proportionate to the amount paid. For students carrying more than the standard schedule the amount refunded for the standard schedule will be in accordance with the above chart, for the extra hours the amount refunded will be proportionate to the extra tuition paid.

For students enrolled in programs which exceed at least one full year of three trimesters but which conclude with less than a full three-trimester year, the percent of tuition refunded for the partial final year will be according to the schedule below. Only those students who began one of these programs at the first academic course level, and who progressed through the program without interruption, are eligible for this refund.

Week of Termination	Partial Final Year Two-term Segment Refunds		
	One-term Segment Refunds	1st Trimester	2nd Trimester
1st week	\$1369	\$1369	\$1369
2nd week	1219	913	913
3rd week	1219	763	763
4th week	1219	763	307
5th-8th weeks	763	763	307
9th-12th weeks	307	307	307
13th-15th weeks	-0-	-0-	-0-

Part-time Night Students with Standard Schedules

If termination is within the	Term of Attendance and Amount of Tuition Refundable					
	1	2	3	4	5	6
1st week	\$848	\$848	\$848	\$848	\$848	\$848
2nd week	565	565	565	565	565	565
3rd week	283	428	283	283	415	283
4th week	152	428	-0-	133	415	-0-
5th-8th weeks	152	428	-0-	133	415	-0-
9th-12th weeks	152	-0-	-0-	133	-0-	-0-
13th-15th weeks	-0-	-0-	-0-	-0-	-0-	-0-

For part-time night students carrying less than 6 credit hours, the tuition refunded will be in accordance with the above chart, but reduced proportionate to the amount paid. For students carrying more than the standard schedule, the amount refunded for the standard schedule will be in accordance with the above chart, but for the extra hours, will be proportionate to the extra tuition paid.

Upon completion of each trimester, the student will be obligated for the total tuition for that trimester.

Standard business methods are used in the collection of delinquent payments.

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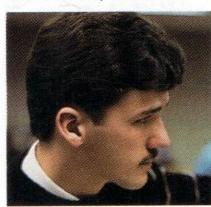
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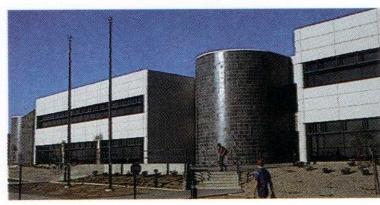
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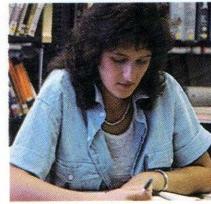
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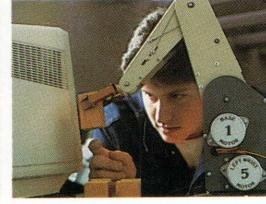
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